

UT7 BLOCK DIAGRAM

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : SVCC
LAYER 5 : IN2
LAYER 6 : IN3
LAYER 7 : SGND1
LAYER 8 : BOT

Cable Docking

VGA
 RJ-45
 CIR/Pwr btn
 SPDIF Out
 Stereo MIC
 Headphone Jack
 USB Port
 VOL Cntr

PAGE 38

SYSTEM CHARGER ISL6251AHAZ-
 PAGE 39

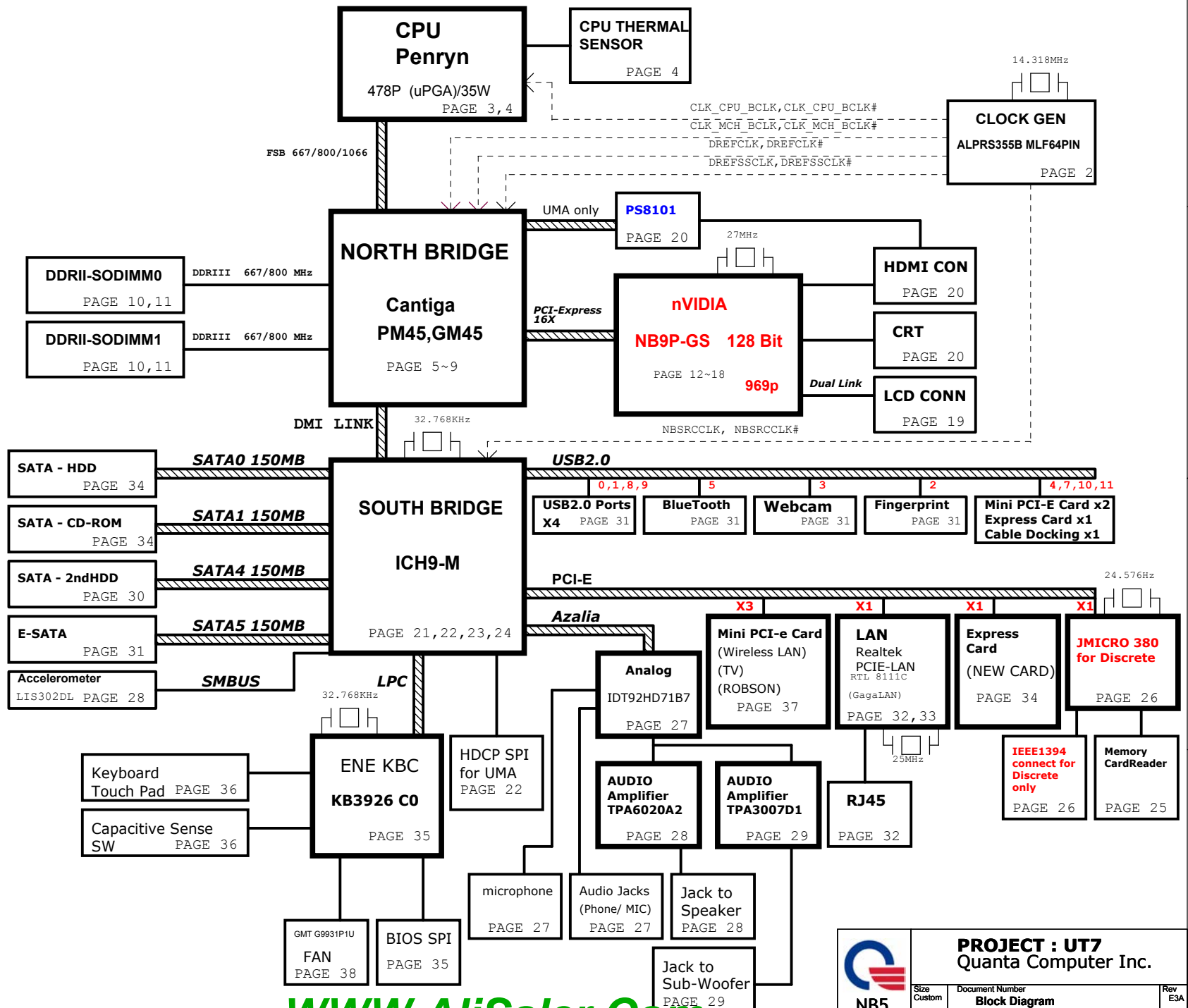
SYSTEM POWER ISL6237IRZ-T
 PAGE 40

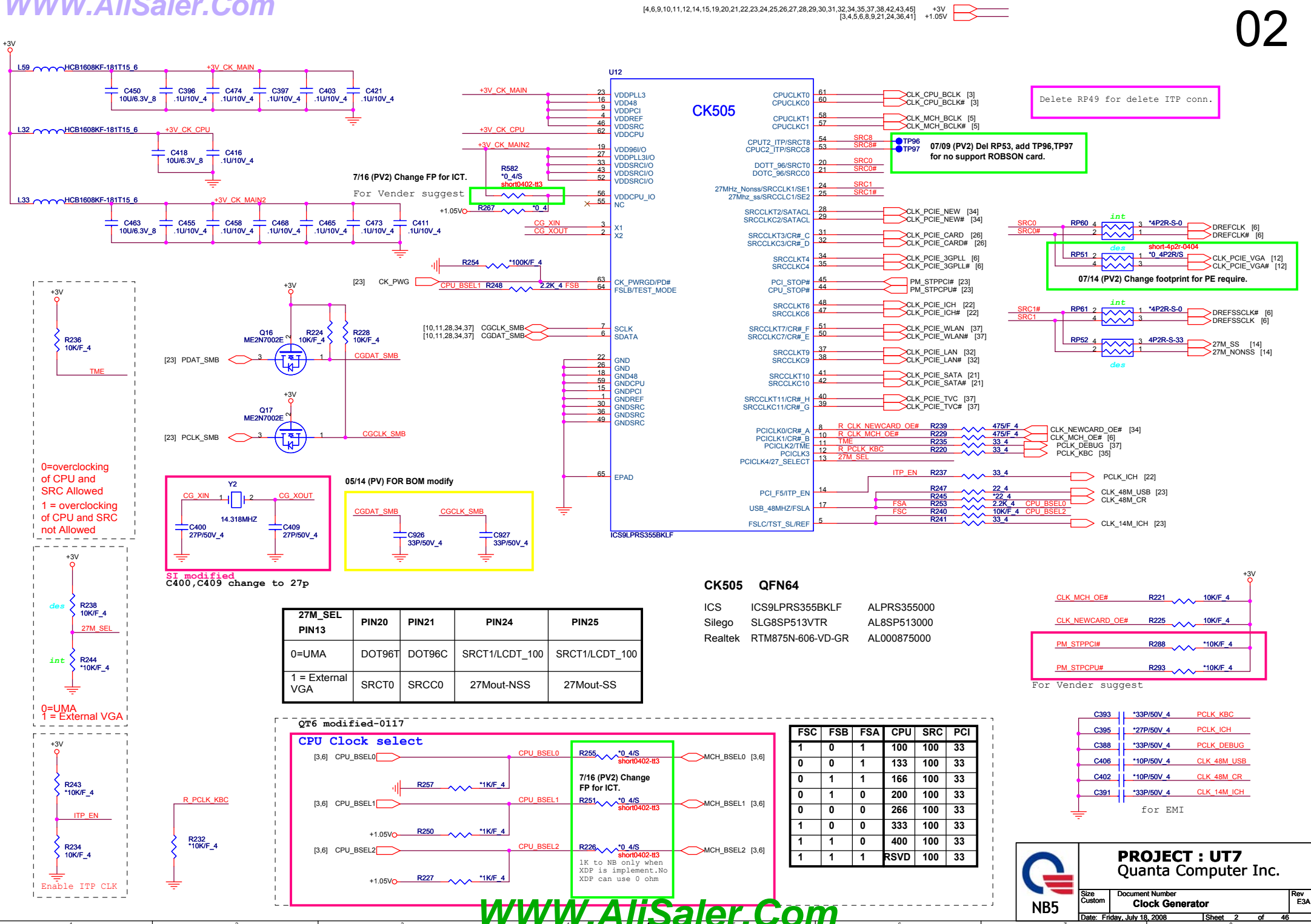
DDR II SMDDR_VTERM
 1.8V/1.8VSUS(TPS51116REGR)
 PAGE 44

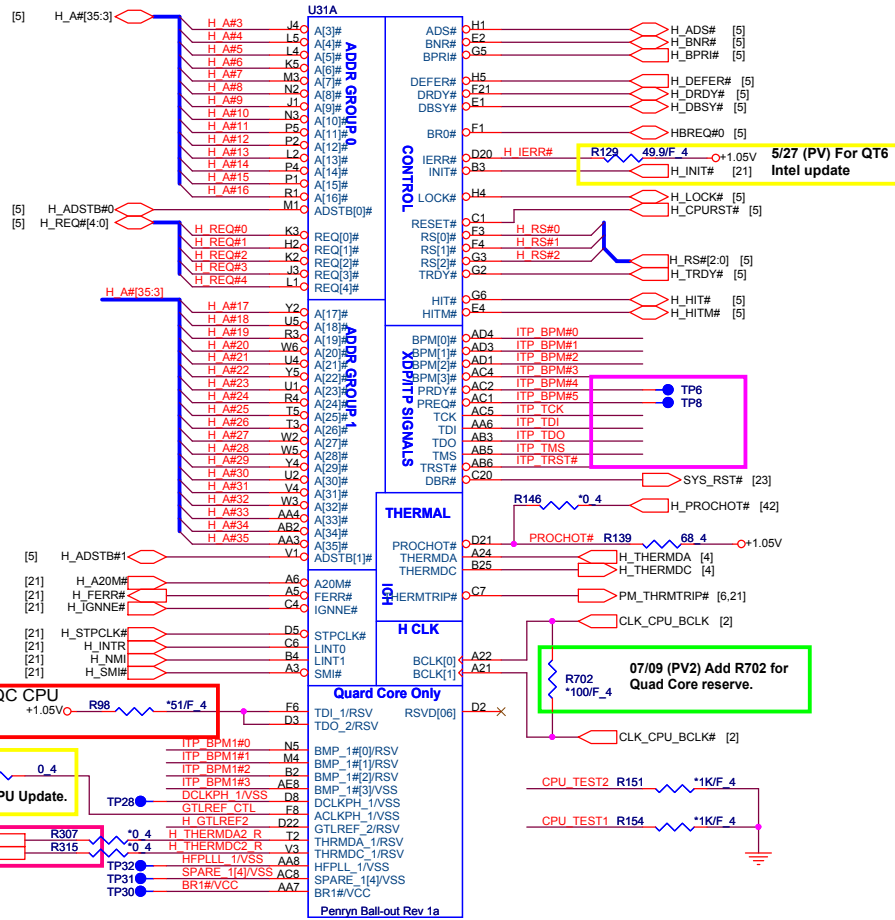
VCCP +1.5V AND GMCH
 1.05V(RT8204)
 PAGE 44

VGACORE(1.025V)Oz8118
 PAGE 43

CPU CORE ISL6266A
 PAGE 42

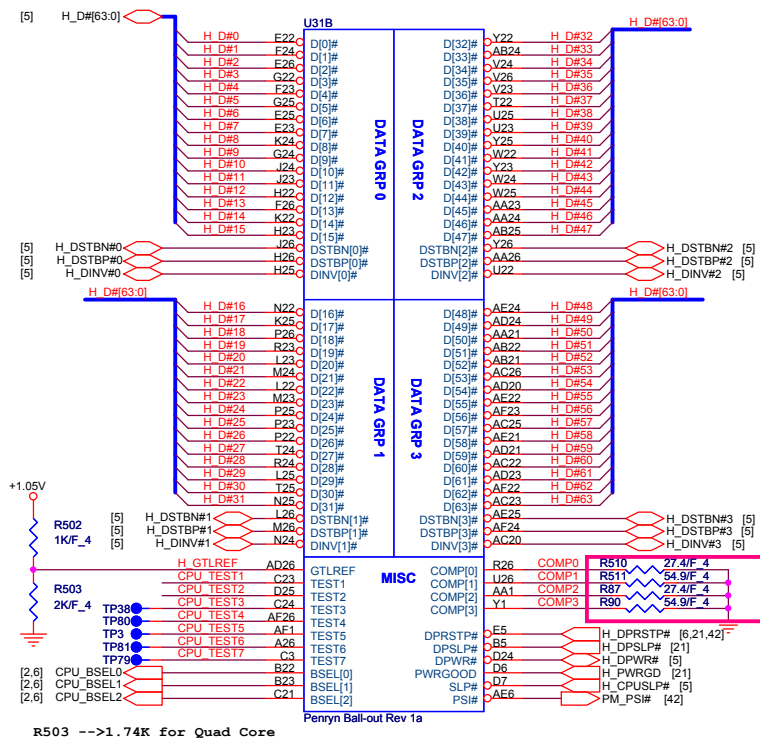
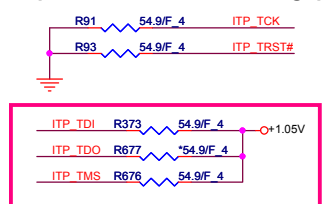






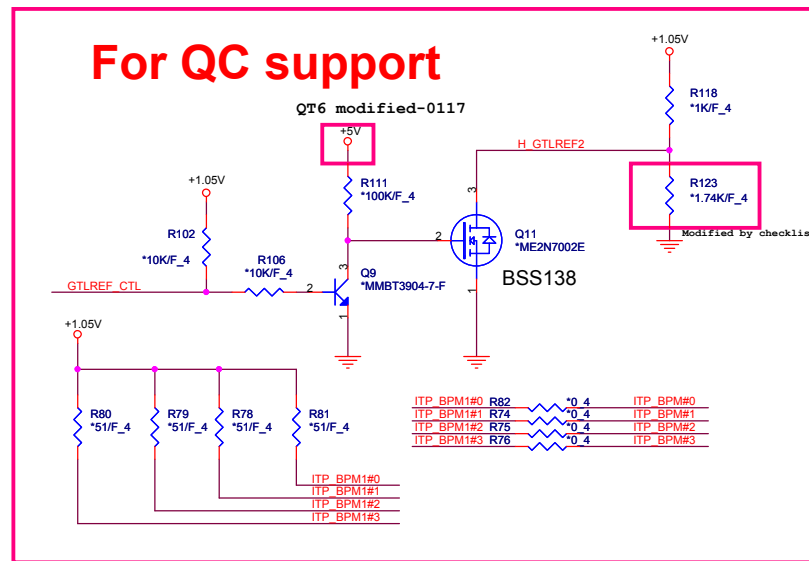
MODEL	UT7 Quad Core	UT6 Dual Core
R696	*0_4	0_4

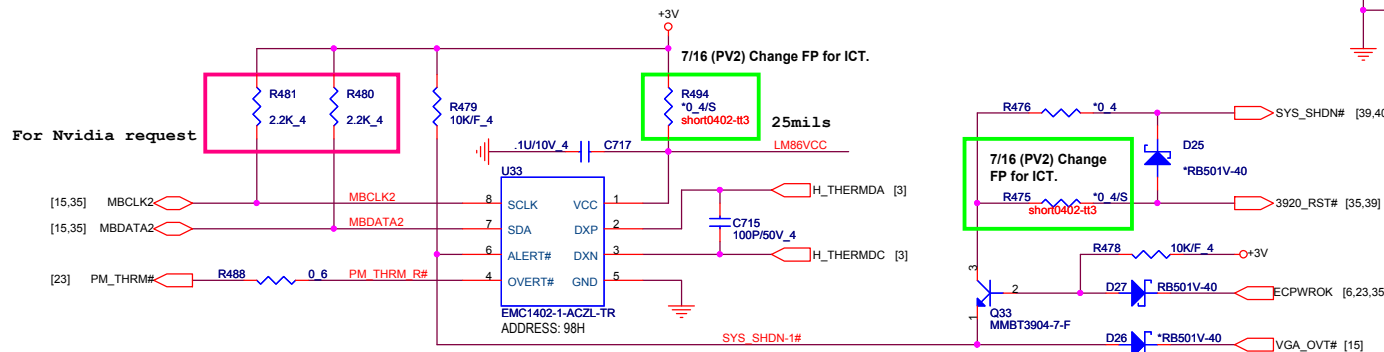
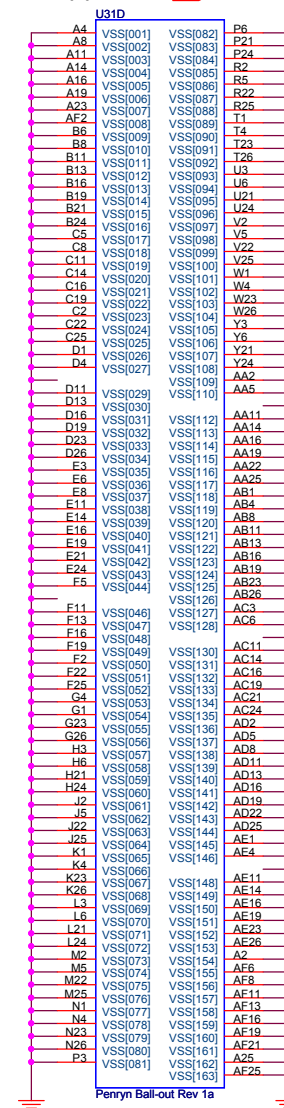
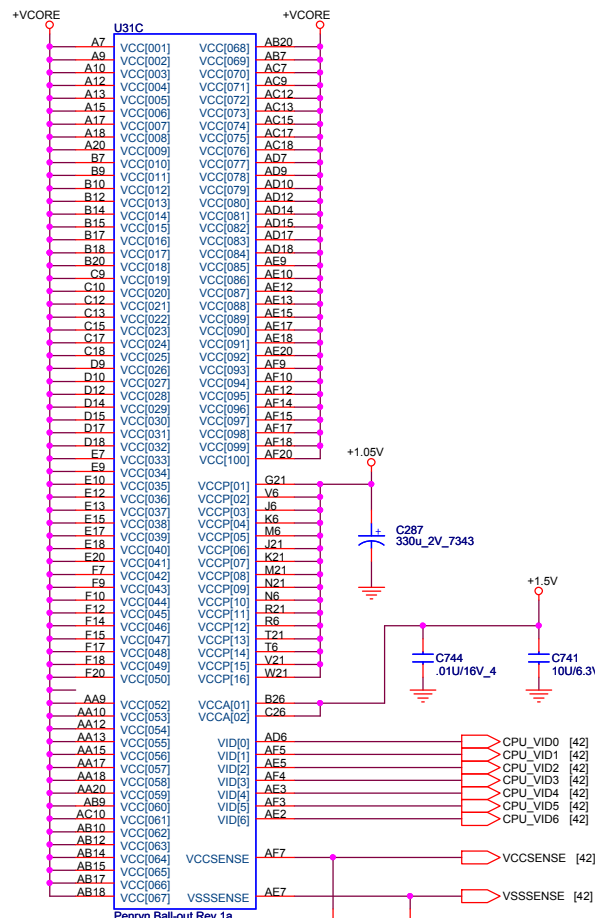
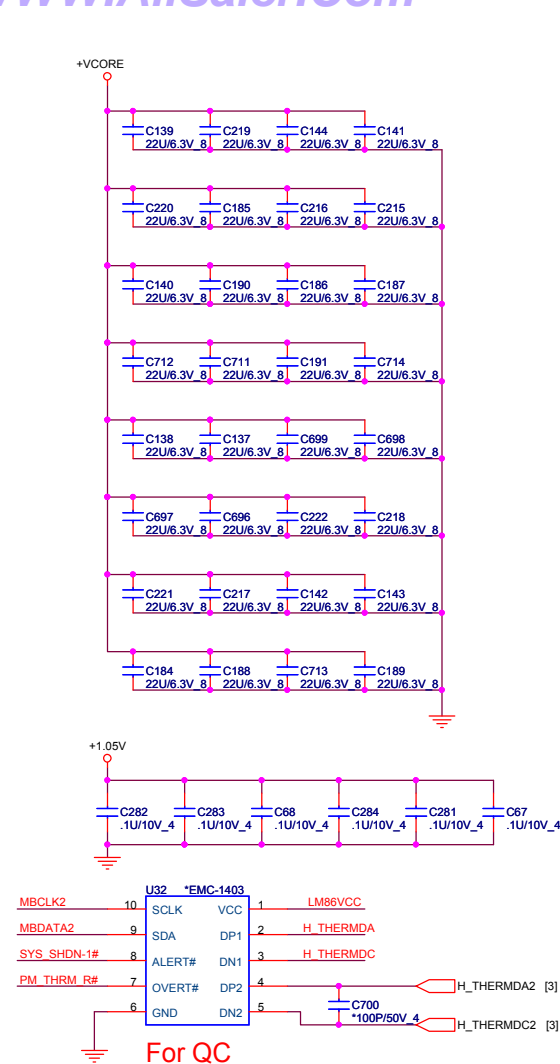
Populate ITP700Flex for bringup



	COMP0/2	COMP1/3
Dual Core	27.4 Ohm (CS02742FB19)	54.9 Ohm (CS05492FB19)
Guard Core	24.9 Ohm (CS02492FB29)	49.9 Ohm (CS04992FB31)

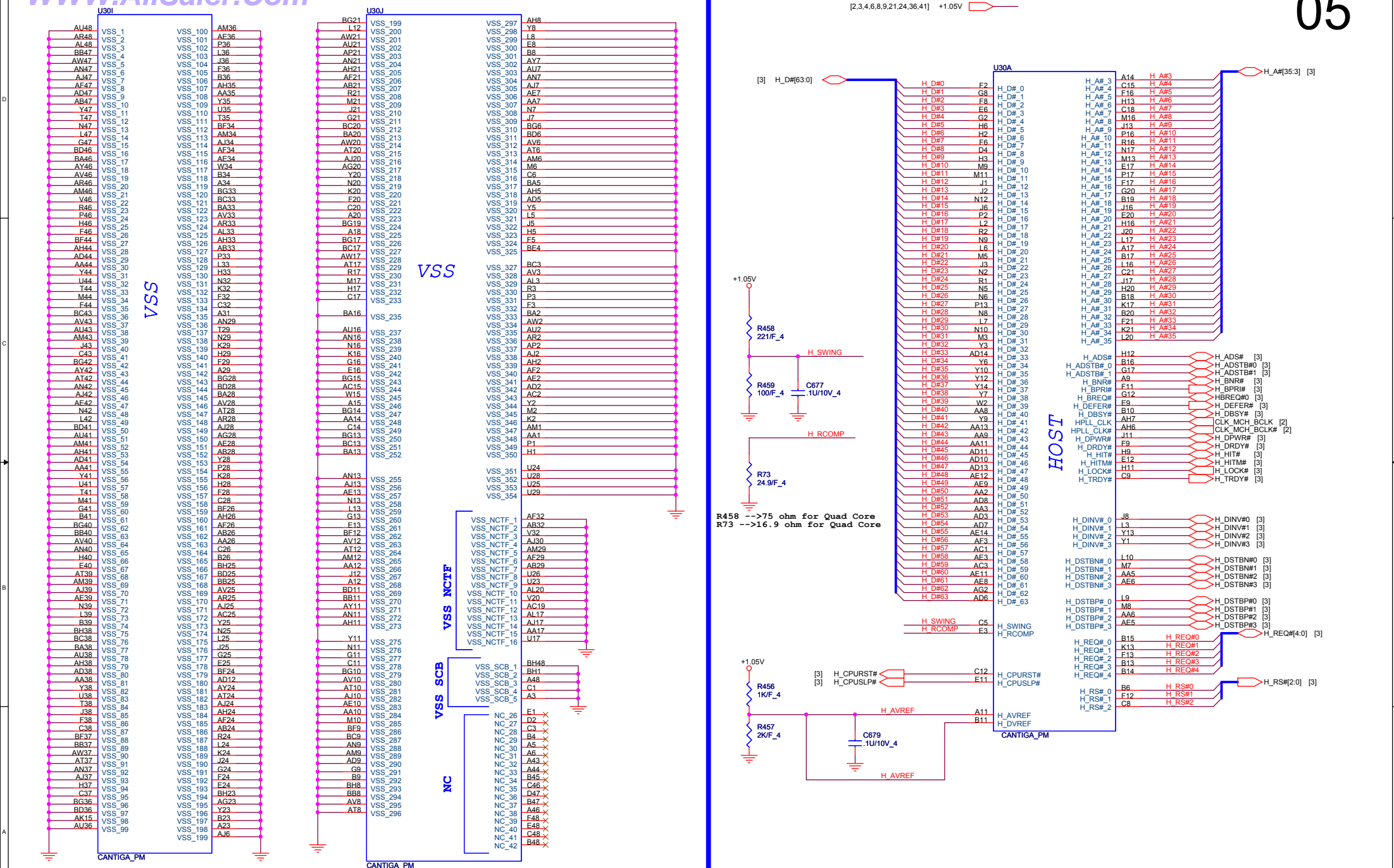
For QC support





PROJECT : UT7
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Penryn & TH Monitor 2/2	E3A
Date: Friday, July 18, 2008	Sheet 4 of 46	



MCH_CFG_5 DMIx2selection
Low = DMI X2
High = DMI X4 (Default)
MCH_CFG_16 FSB Dynamic ODT
Low = Dynamic ODT disabled
High = Dynamic ODT enabled (default)

MCH_CFG_9 PCI Express Graphic Lane
Low: Reverse Lane
High: Normal operation(Default)

MCH_CFG_19 DMI Lane Reversal
Low = Normal operation (Default)
High = Reverse Lanes

MCH_CFG_6 ITPM Host Interface
Low = The ITPM Host Interface is enabled
High = The ITPM Host Interface is disabled (default)

MCH_CFG_7 Intel (R) Management Engine Crypto
Low: Intel (R) Management Engine Crypto
High: Intel (R) Management Engine Crypto

MCH_CFG_10 PCIe Lookback Enable
Low = Enabled
High: Disabled (Default)

MCH_CFG_12/13 XOR/ALLZ/CLOCK Un-gating

MCH_CFG_13 MCH_CFG_12 Configuration

0	0	Reserved
1	0	XOR Mode enabled
0	1	All-Z Mode enabled
1	1	Normal operation (Default)

MCH_CFG2:0
000 = FSB1066
010 = FSB800
011 = FSB667
Others = Reserved

Digital Display Port (SDVO/DP/HDMI) Concurrent with PCIe
Low = Only digital display port (SDVO/DP/HDMI) or PCIe is operational (default)
High = Digital display port (SDVO/DP/HDMI) and PCIe are operating simultaneously via the PEG port

MCH_CFG_20

TP34 AL34
TP33 AK34
TP36 AN35
TP35 AM35

[2,3] MCH_BSEL0
[2,3] MCH_BSEL1
[2,3] MCH_BSEL2

TP111 MCH_CFG_3
TP244 MCH_CFG_4
TP777 MCH_CFG_5
TP114 MCH_CFG_6
TP788 MCH_CFG_7
TP766 MCH_CFG_8
TP272 MCH_CFG_9
TP177 MCH_CFG_10
TP233 MCH_CFG_11
TP255 MCH_CFG_12
TP188 MCH_CFG_13
TP166 MCH_CFG_14
TP122 MCH_CFG_15
TP777 MCH_CFG_16
TP222 MCH_CFG_17
TP266 MCH_CFG_18
TP266 MCH_CFG_19
TP277 MCH_CFG_20

[2,3] PM_SYNC#
[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST#
[3,21] PM_THRMTRIP#
[23,42] DPRSLPVR

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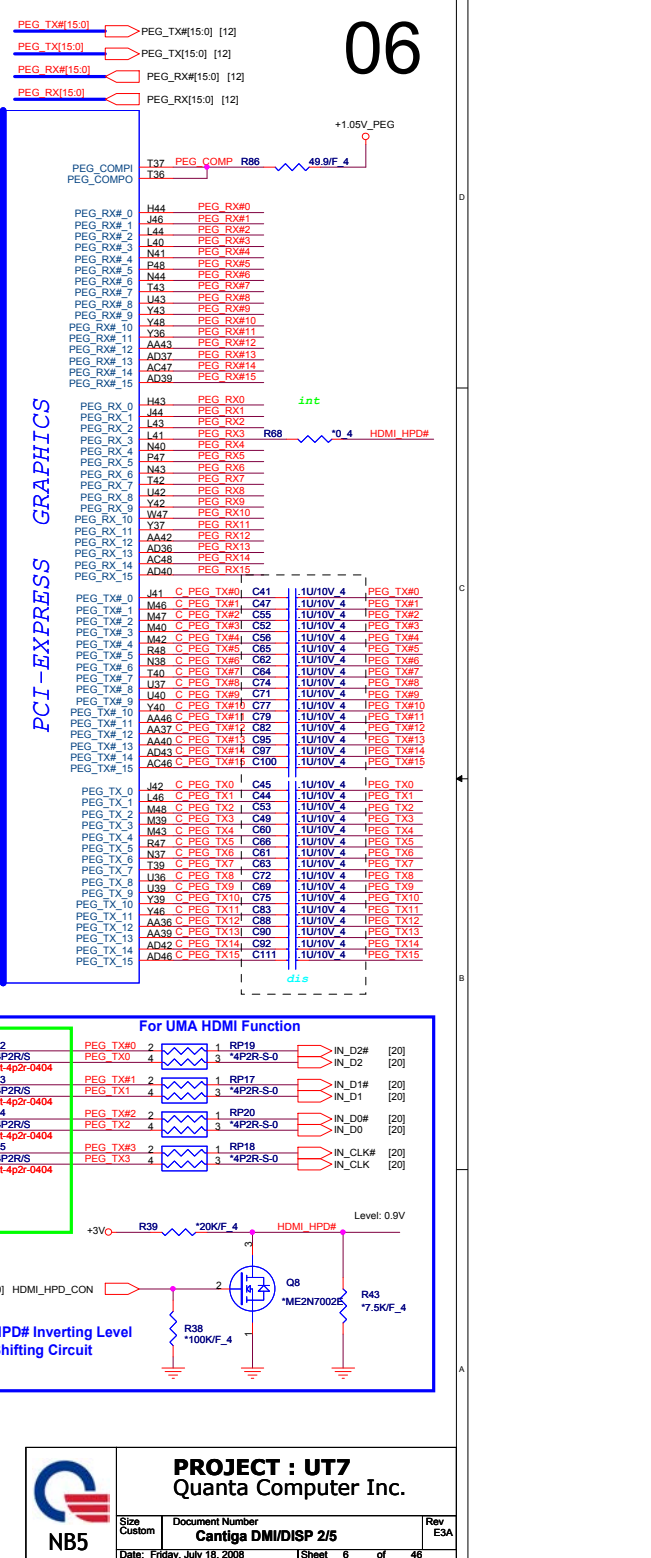
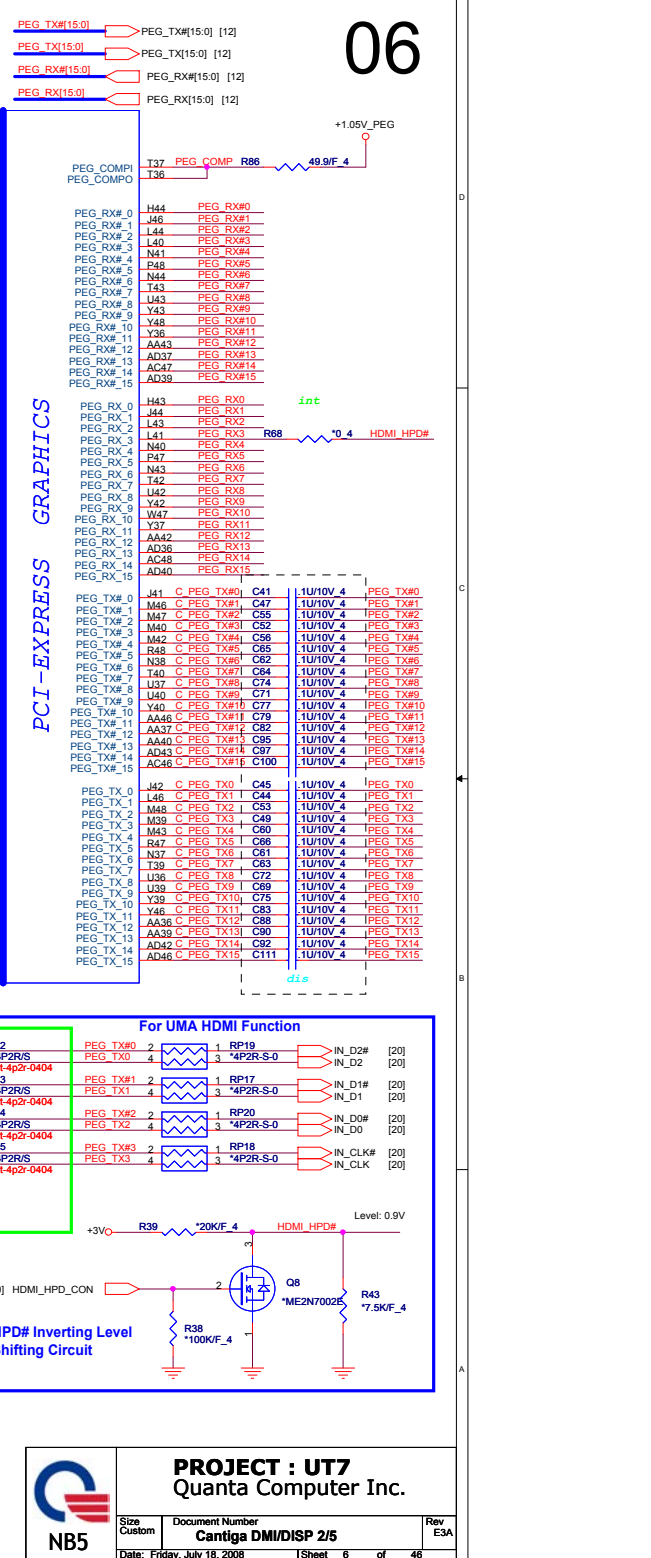
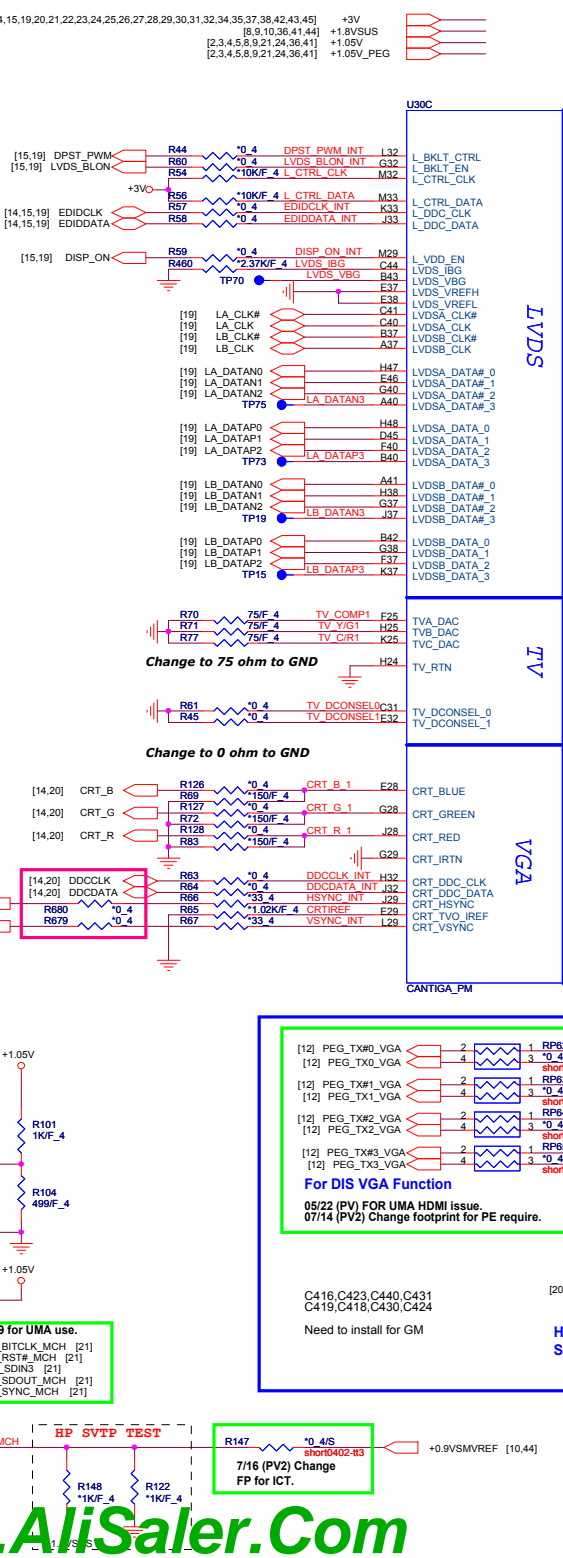
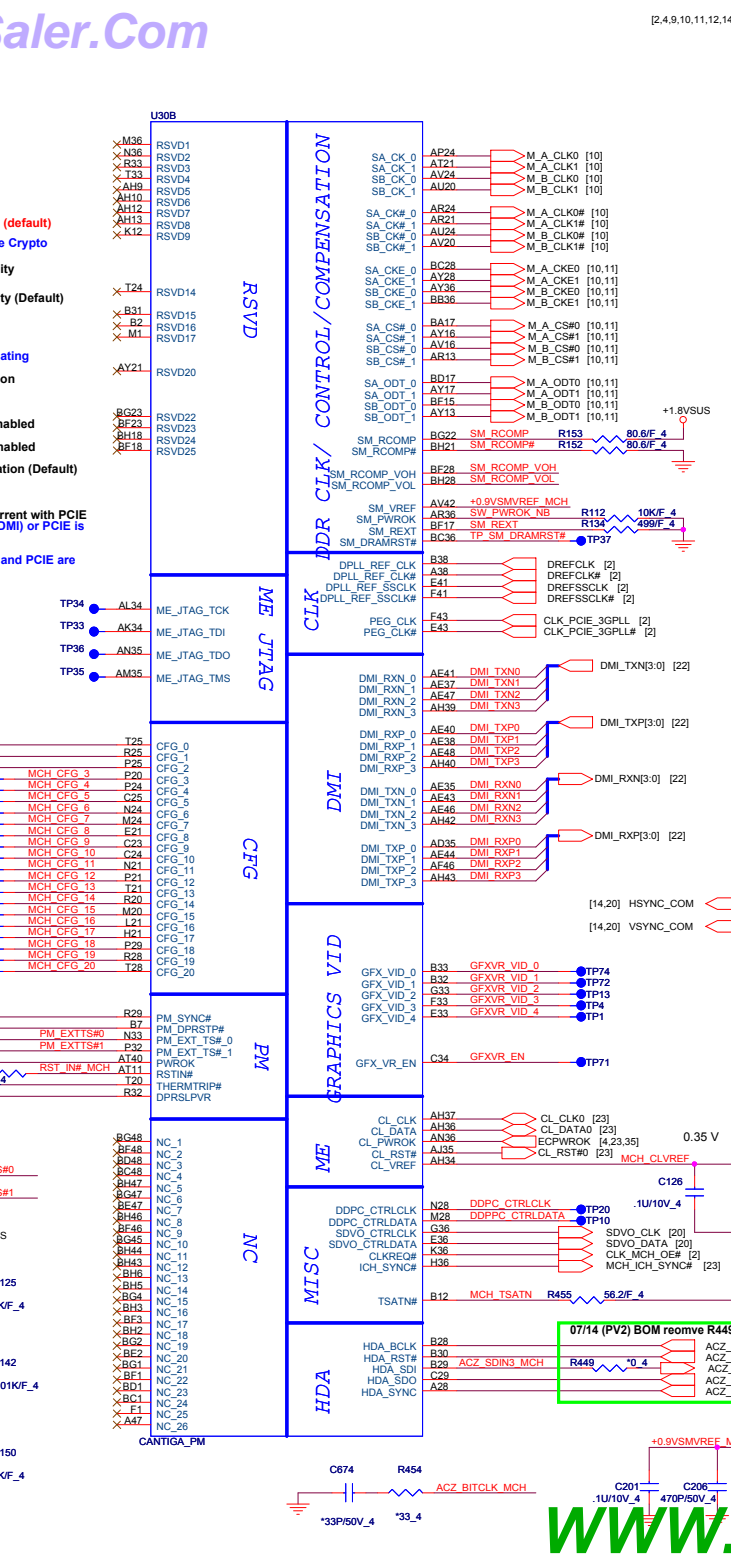
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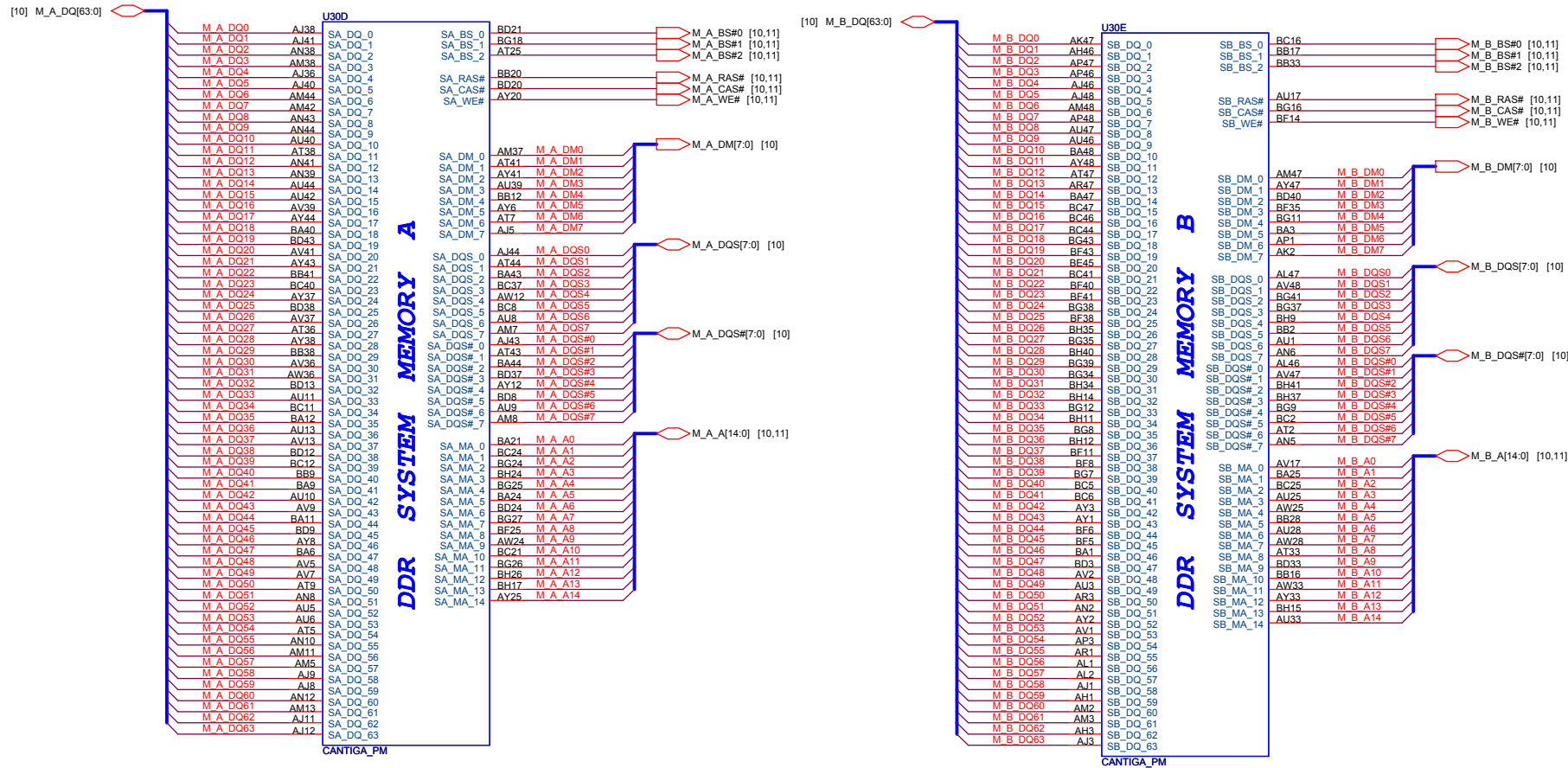
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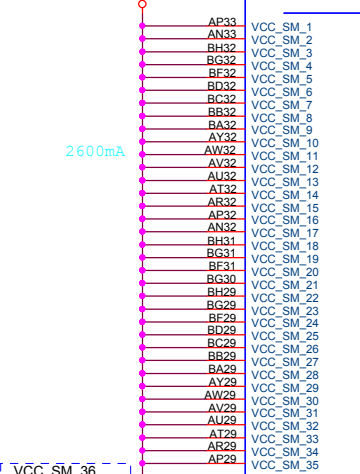
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PROJECT : UT7
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Size Custom	Document Number Cantiga DDR2 3/5	Rev E3A
Date: Friday, July 18, 2008	Sheet 7 of 46	



POWER

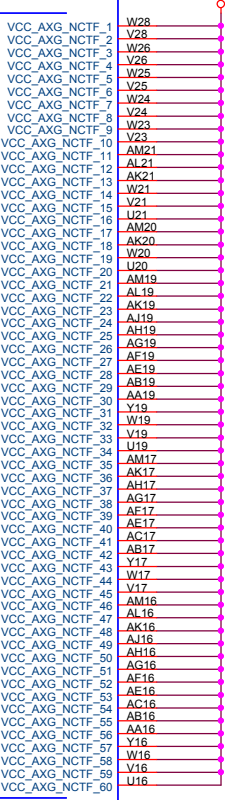
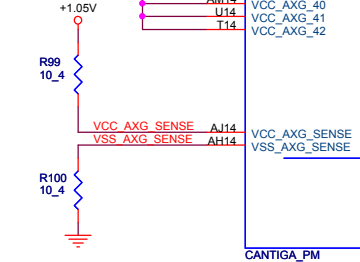
VCC SM

VCC GFX NCTF

VCC GFX

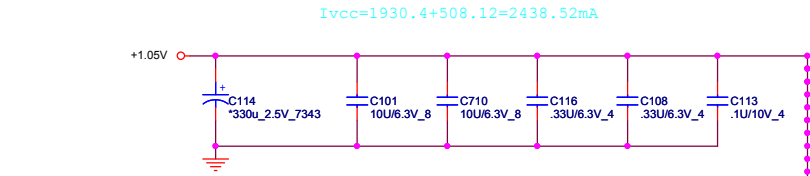
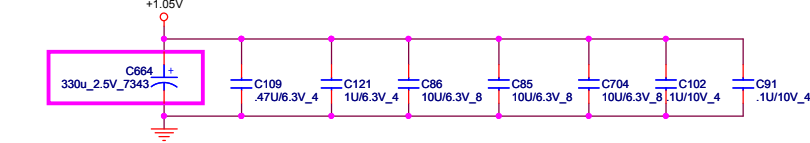
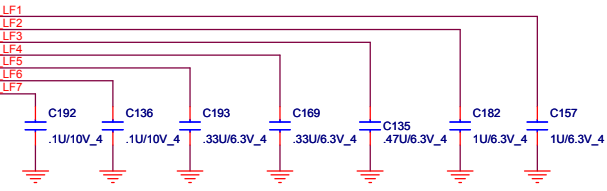
VCC SM LF

CANTIGA_PM

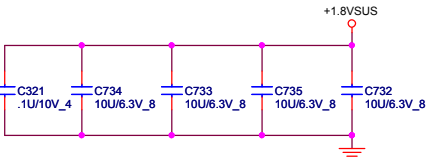


Ivcc_axg=6326.84mA

Change footprint

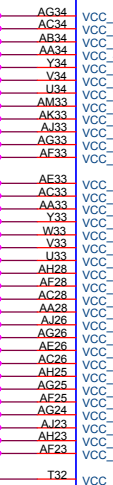


Ivcc=1930.4+508.12=2438.52mA



[6,9,10,36,41,44] +1.8VSUS
[2,3,4,5,6,9,21,24,36,41] +1.05V

U30F

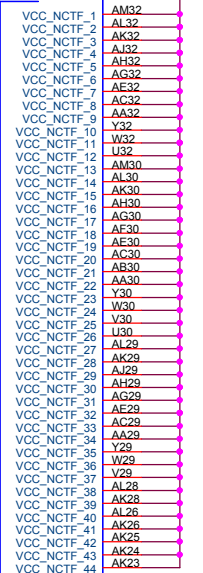


POWER

VCC CORE

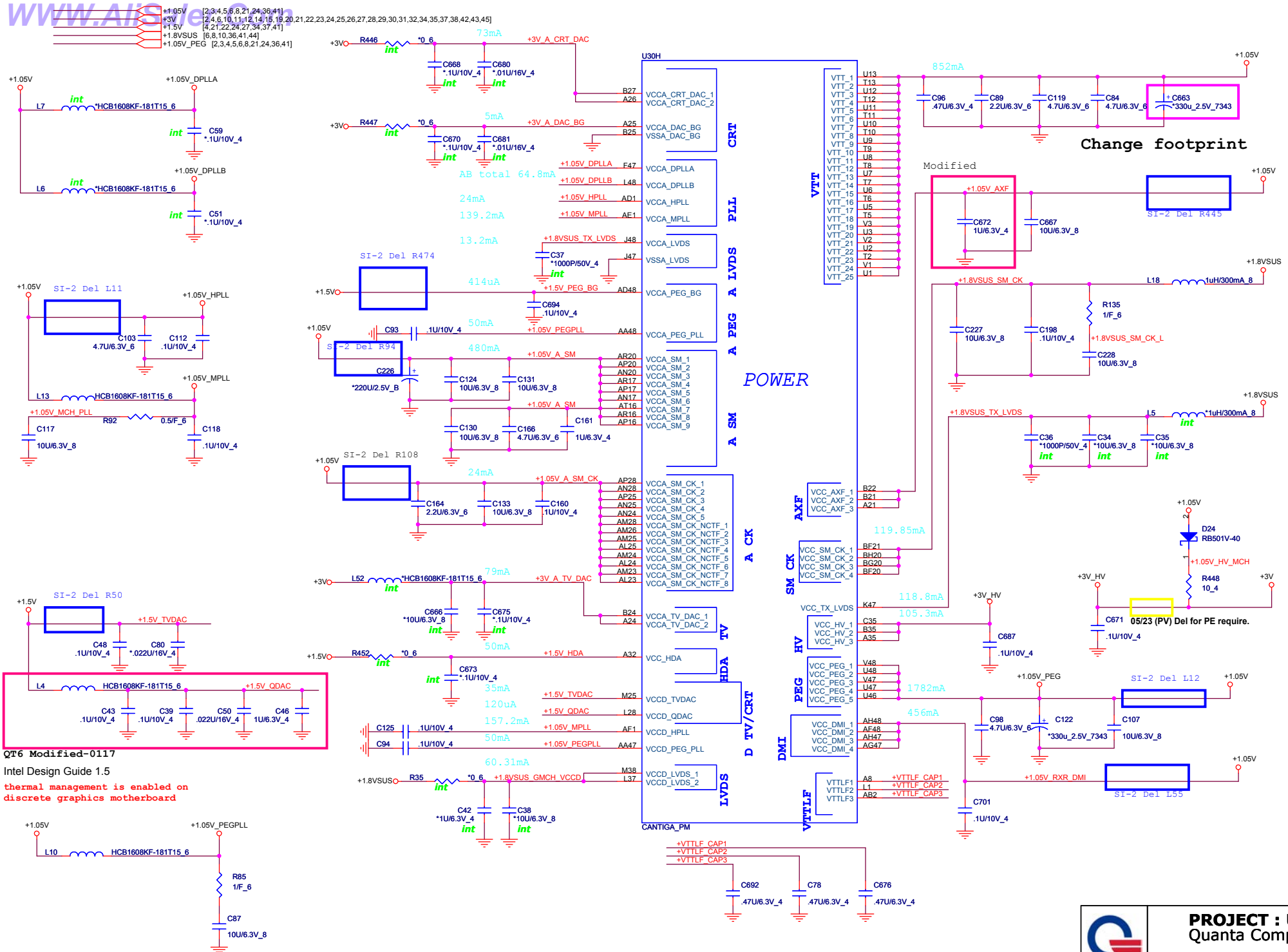
VCC NCTF

CANTIGA_PM



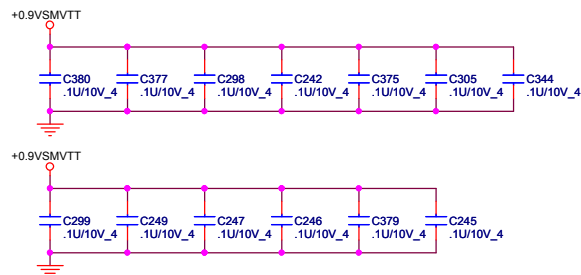
PROJECT : UT7
Quanta Computer Inc.

Size Custom	Document Number	Rev E3A
	Cantiga Vcc 4/5	
Date: Friday, July 18, 2008	Sheet 8 of 46	



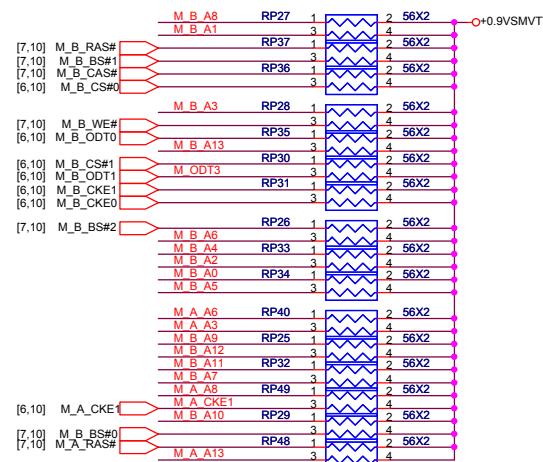
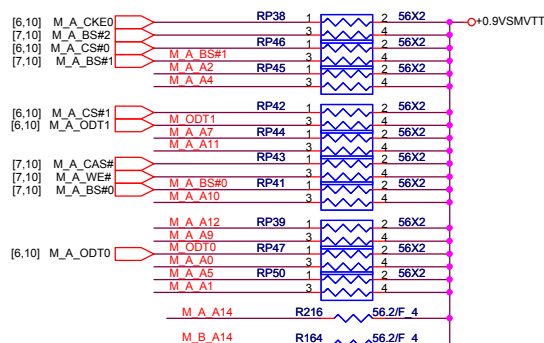
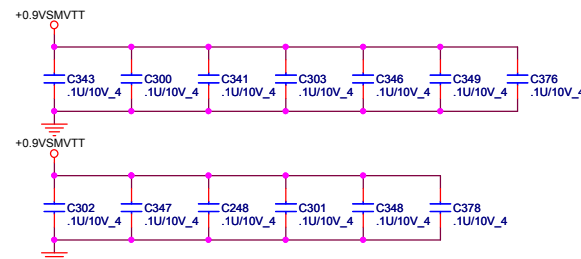
DDRII DUAL CHANNEL A,B.

DDRII A CHANNEL

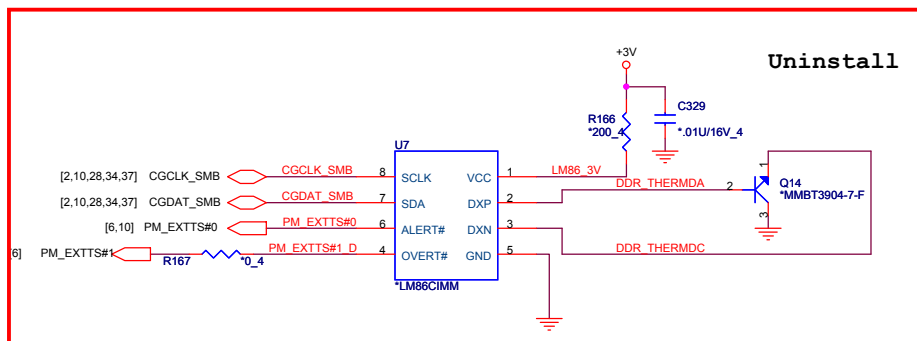


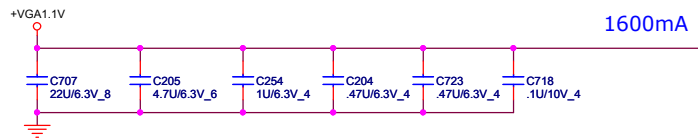
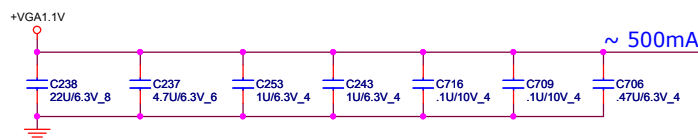
Layout note: Place one cap close to every 2 pullup resistors terminated to SMDR_VTERM

DDRII B CHANNEL

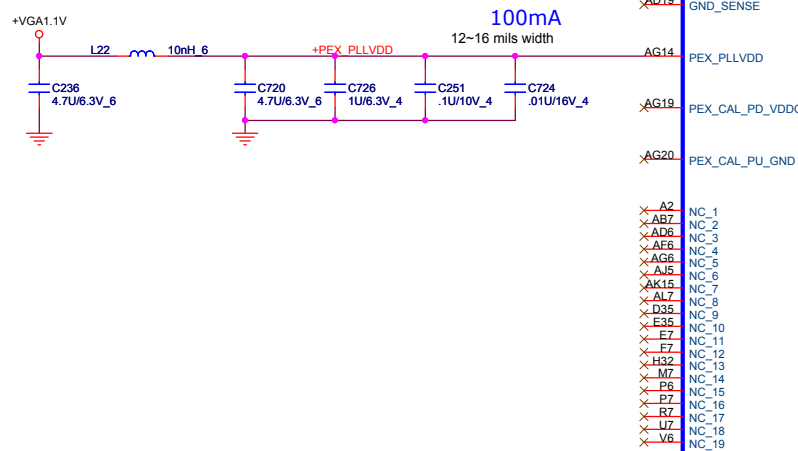
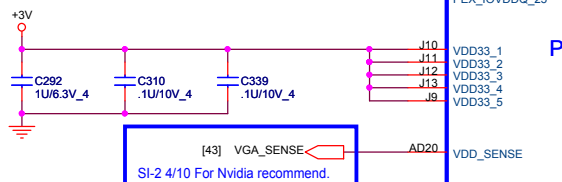


M_B_A[14..0] M_B_A[14..0] [7,10]
M_A_A[14..0] M_A_A[14..0] [7,10]





Near BGA



[2,4,6,9,10,11,14,15,19,20,21,22,23,24,25,26,27,28,29,30,31,32,34,35,37,38,42,43,45]

+3V
[13,14,44] +VGA1.1V

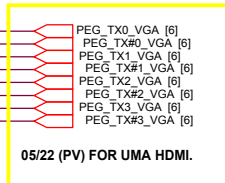
U35A
BGA689-NVIDIA-NB8P-GS
COMMON

AK16
AK17
AK21
AK24
AK27
AG11
AG12
AG13
AG15
AG16
AG17
AG18
AG22
AG23
AG24
AG25
AG26
AJ14
AJ15
AJ19
AJ21
AJ22
AJ24
AJ25
AJ27
AK18
AK20
AK23
AK26
AL16

PCI EXPRESS

PEX_RX0
PEX_RX0*
PEX_RX1
PEX_RX1*
PEX_RX2
PEX_RX2*
PEX_RX3
PEX_RX3*
PEX_RX4
PEX_RX4*
PEX_RX5
PEX_RX5*
PEX_RX6
PEX_RX6*
PEX_RX7
PEX_RX7*
PEX_RX8
PEX_RX8*
PEX_RX9
PEX_RX9*
PEX_RX10
PEX_RX10*
PEX_RX11
PEX_RX11*
PEX_RX12
PEX_RX12*
PEX_RX13
PEX_RX13*
PEX_RX14
PEX_RX14*
PEX_RX15
PEX_RX15*

AP17 PEG TX0 VGA
AN17 PEG TX#0 VGA
AN19 PEG TX1 VGA
AP19 PEG TX#1 VGA
AR19 PEG TX2 VGA
AR20 PEG TX#2 VGA
AP20 PEG TX3 VGA
AN20 PEG TX#3 VGA
AN22 PEG TX4
AP22 PEG TX#4
AR22 PEG TX5
AR23 PEG TX#5
AP23 PEG TX6
AR23 PEG TX#6
AN25 PEG TX7
AP25 PEG TX#7
AR25 PEG TX8
AR26 PEG TX#8
AP26 PEG TX9
AN26 PEG TX#9
AN28 PEG TX10
AP28 PEG TX#10
AR28 PEG TX11
AR29 PEG TX#11
AP29 PEG TX12
AN29 PEG TX#12
AN31 PEG TX13
AP31 PEG TX#13
AR31 PEG TX14
AR32 PEG TX#14
AR34 PEG TX15
AP34 PEG TX#15



PEX_TX0
PEX_TX0*
PEX_TX1
PEX_TX1*
PEX_TX2
PEX_TX2*
PEX_TX3
PEX_TX3*
PEX_TX4
PEX_TX4*
PEX_TX5
PEX_TX5*
PEX_TX6
PEX_TX6*
PEX_TX7
PEX_TX7*
PEX_TX8
PEX_TX8*
PEX_TX9
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PEX_TX11
PEX_TX11*
PEX_TX12
PEX_TX12*
PEX_TX13
PEX_TX13*
PEX_TX14
PEX_TX14*
PEX_TX15
PEX_TX15*

AL17 C PEG RX0 C171
AM17 C PEG RX#0 C170
AM18 C PEG RX1 C209
AM19 C PEG RX#1 C210
AL19 C PEG RX2 C172
AK19 C PEG RX#2 C173
AL20 C PEG RX3 C145
AM20 C PEG RX#3 C146
AM21 C PEG RX4 C211
AM22 C PEG RX#4 C212
AL22 C PEG RX5 C213
AK22 C PEG RX#5 C214
AL23 C PEG RX6 C147
AM23 C PEG RX#6 C148
AM24 C PEG RX7 C174
AM25 C PEG RX#7 C175
AL25 C PEG RX8 C149
AK25 C PEG RX#8 C150
AL26 C PEG RX9 C176
AM26 C PEG RX#9 C177
AM27 C PEG RX10 C151
AM28 C PEG RX#10 C152
AL28 C PEG RX11 C199
AK28 C PEG RX#11 C200
AK29 C PEG RX12 C178
AL29 C PEG RX#12 C179
AM29 C PEG RX13 C162
AM30 C PEG RX#13 C163
AM31 C PEG RX14 C155
AM32 C PEG RX#14 C156
AN32 C PEG RX15 C180
AP32 C PEG RX#15 C181

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PEG_RX#0 [6]
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PEG_RX#1 [6]
PEG_RX2 [6]
PEG_RX#2 [6]
PEG_RX3 [6]
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PEG_RX#7 [6]
PEG_RX8 [6]
PEG_RX#8 [6]
PEG_RX9 [6]
PEG_RX#9 [6]
PEG_RX10 [6]
PEG_RX#10 [6]
PEG_RX11 [6]
PEG_RX#11 [6]
PEG_RX12 [6]
PEG_RX#12 [6]
PEG_RX13 [6]
PEG_RX#13 [6]
PEG_RX14 [6]
PEG_RX#14 [6]
PEG_RX15 [6]
PEG_RX#15 [6]

PEX_REFCLK
PEX_REFCLK*

AR16 CLK_PCIE_VGA
AR17 CLK_PCIE_VGA#

CLK_PCIE_VGA [2]
CLK_PCIE_VGA# [2]

PEX_TSTCLK_OUT
PEX_TSTCLK_OUT*

AJ17 R161 *200_4
AJ18

PEX_RST*

AM16 VGA_RST# R491 100/F_4
PLT_RST-R#-1

PEX_CLKREQ*

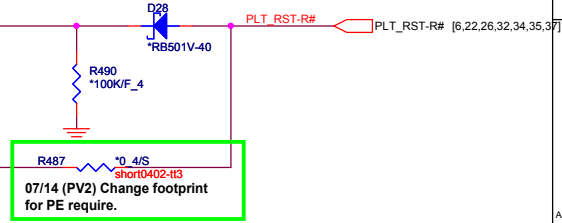
AR13 PEX_CLKREQ# T5

PEX_TERM

AG21 PEX_TERM R165 2.49K/F_4

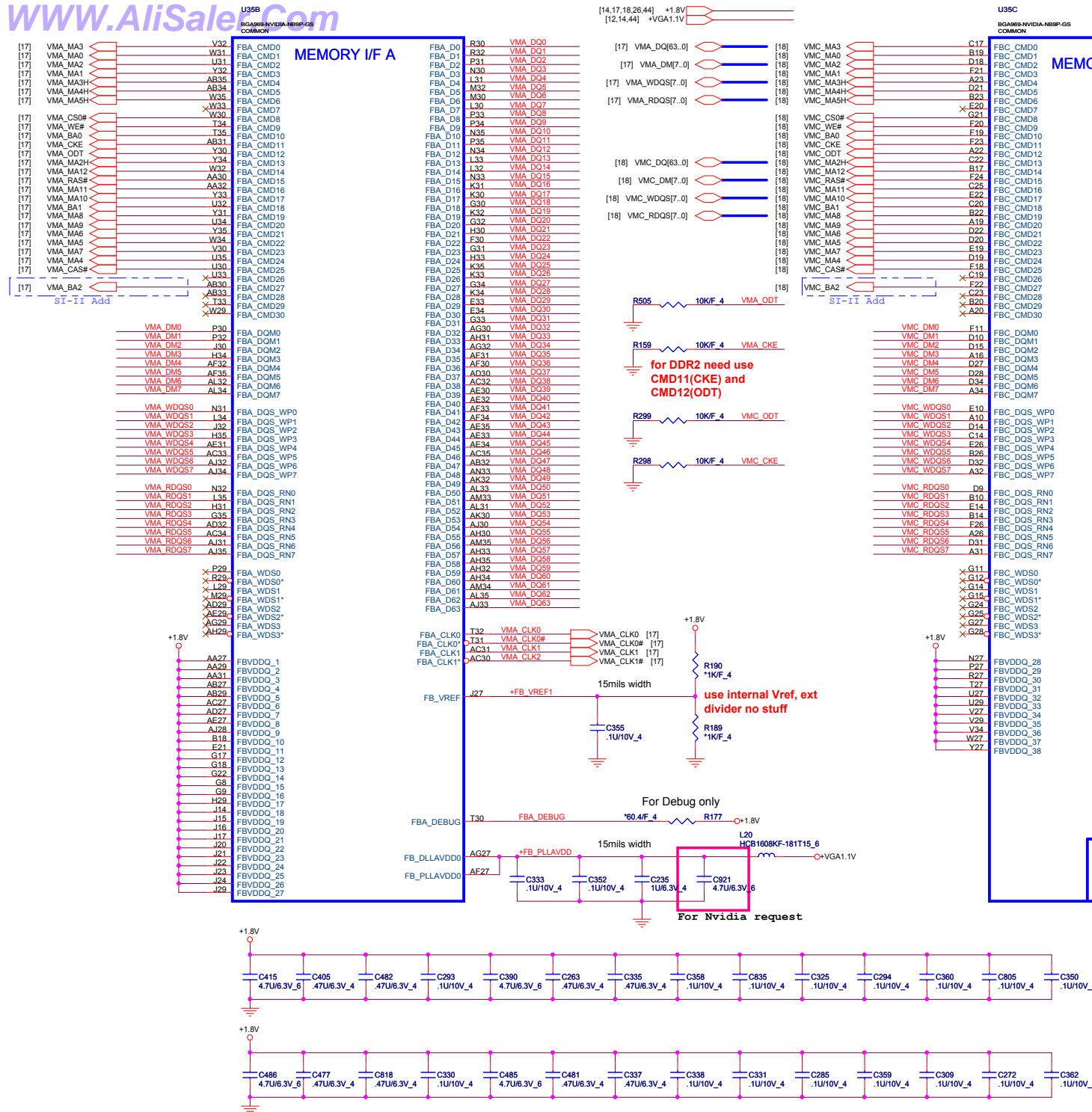
TESTMODE

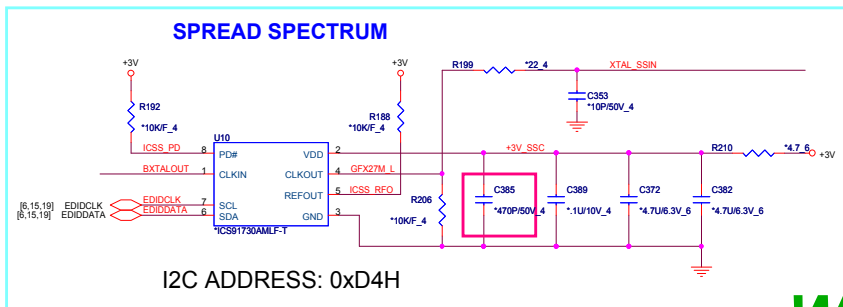
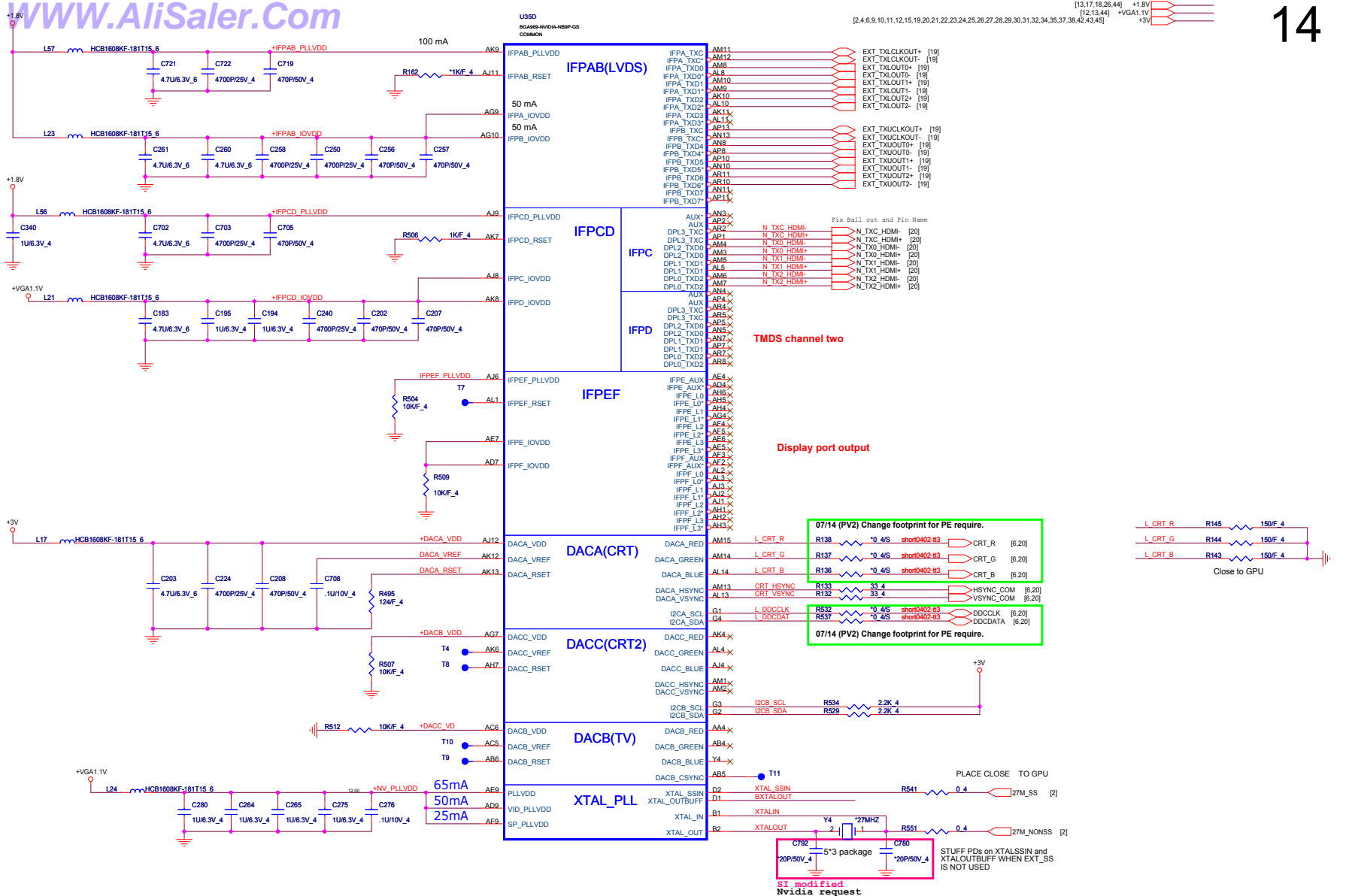
AP35 TESTMODE R486 10K/F_4



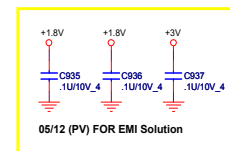
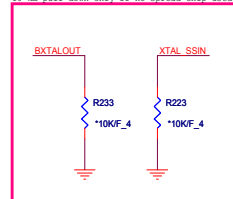
PROJECT : UT7
Quanta Computer Inc.

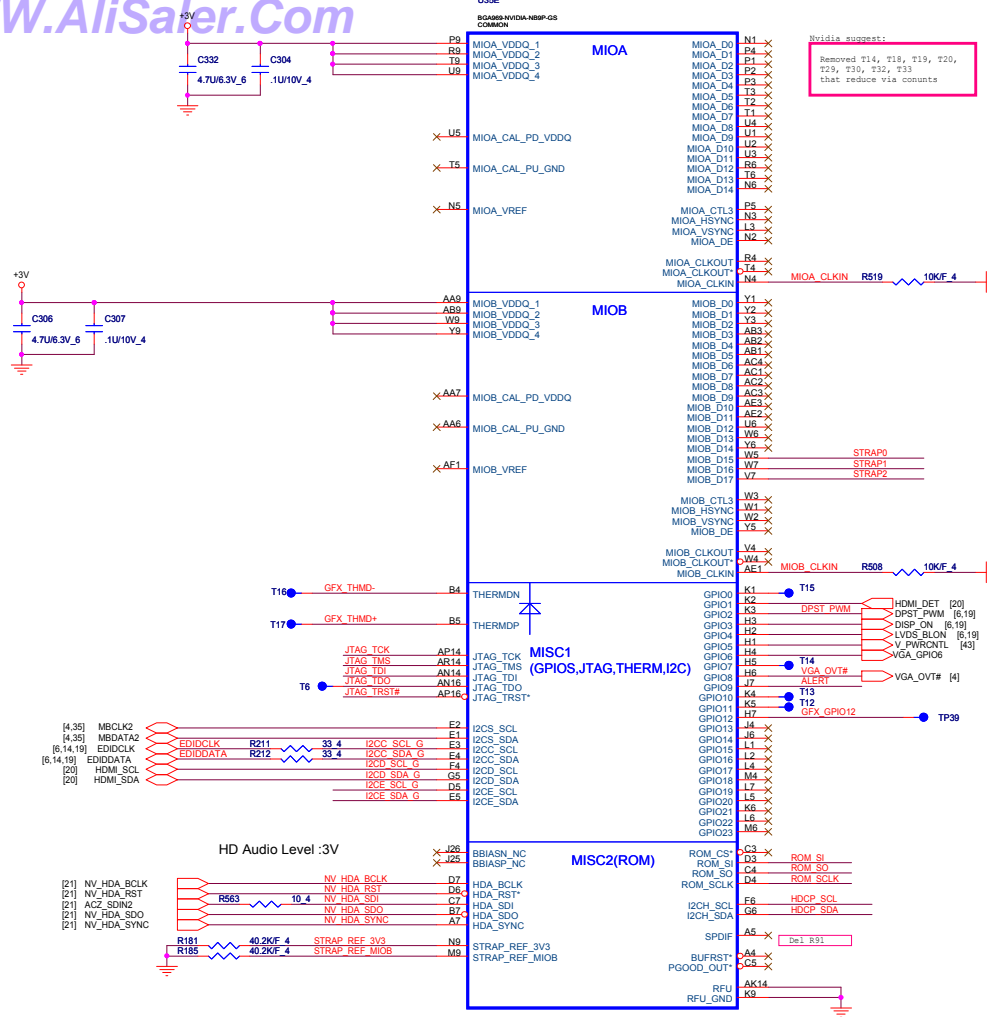
Size Custom	Document Number NV9X (PCIE I/F) 1/5	Rev E3A
Date: Friday, July 18, 2008	Sheet 12 of 46	



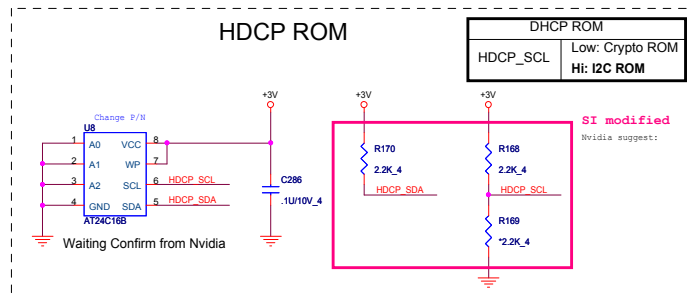


Widia suggest:
10 kΩ pull-down only if no spread chip used.



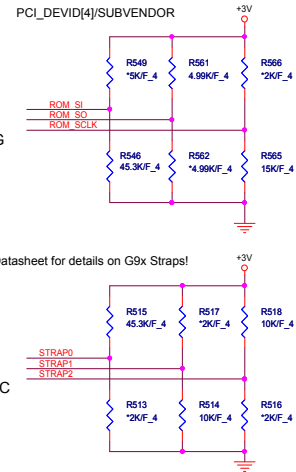


Delete VGA thermal circuit



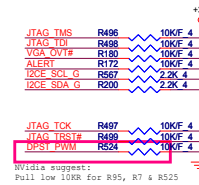
NB9P-GS (G96) Straps
NB9M-GE (G98) Straps
GPIO ASSIGNMENTS

GPIO	I/O	ACTIVE	USAGE
0	IN	N/A	PRIMARY DVI HOTPLUG
1	IN	N/A	SECONDARY DVI HOTPLUG
2	OUT	HIGH	PANEL BACKLIGHT PWM
3	OUT	HIGH	PANEL POWER ENABLE
4	OUT	HIGH	PANEL BACKLIGHT ENABLE
5	OUT	N/A	NVDD VID0
6	OUT	N/A	NVDD VID1
7	OUT	N/A	FBVDD VID0
8	IN	LOW	THERMAL ALERT
9	OUT	LOW	FAN PWM
10	OUT	N/A	FBVREF SELECT
11	OUT	N/A	SLI SYNC0
12	IN	N/A	AC DETECT
13	OUT	LOW	PS CONTROL OR HDMI_CEC
14	OUT	HIGH	PS CONTROL



Logical Strap Bit Mapping

	PU-VDD	PD
5K	1000	0000
10K	1001	0001
15K	1010	0010
20K	1011	0011
25K	1100	0100
30K	1101	0101
35K	1110	0110
45K	1111	0111



	Logical Strapping Bit3	Logical Strapping Bit2	Logical Strapping Bit1	Logical Strapping Bit0
ROM_SO	XCLK_277	TVMODE[2]	TVMODE[1]	TVMODE[0]
ROM_SCLK	PCI_DEVICE[4]	SUB_VENDOR	SLOT_CLK_CFG	PEX_PLL_EN_TERM100
ROM_SI	RAMCFG[3]	RAMCFG[2]	RAMCFG[1]	RAMCFG[0]
STRAP2	PCI_DEVICE[3]	PCI_DEVICE[2]	PCI_DEVICE[1]	PCI_DEVICE[0]
STRAP1	3GIO_PADCFG[3]	3GIO_PADCFG[2]	3GIO_PADCFG[1]	3GIO_PADCFG[0]
STRAP0	USER[3]	USER[2]	USER[1]	USER[0]

PCI_DEVID: STRAP2

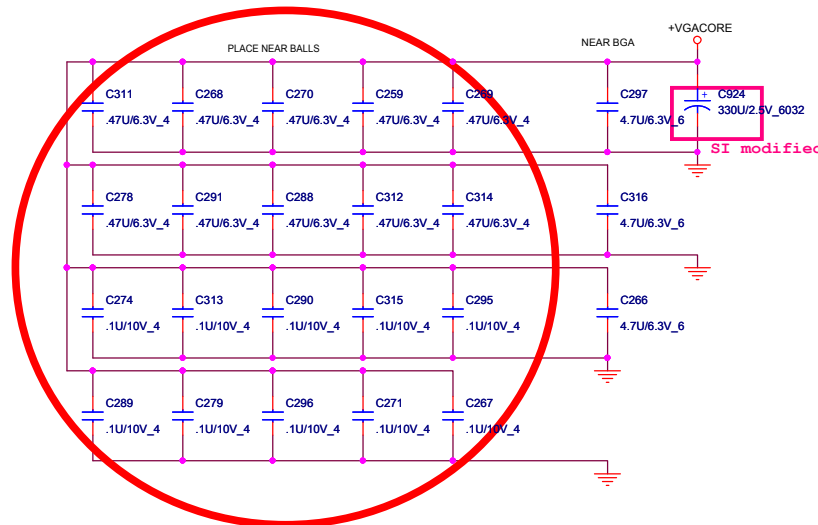
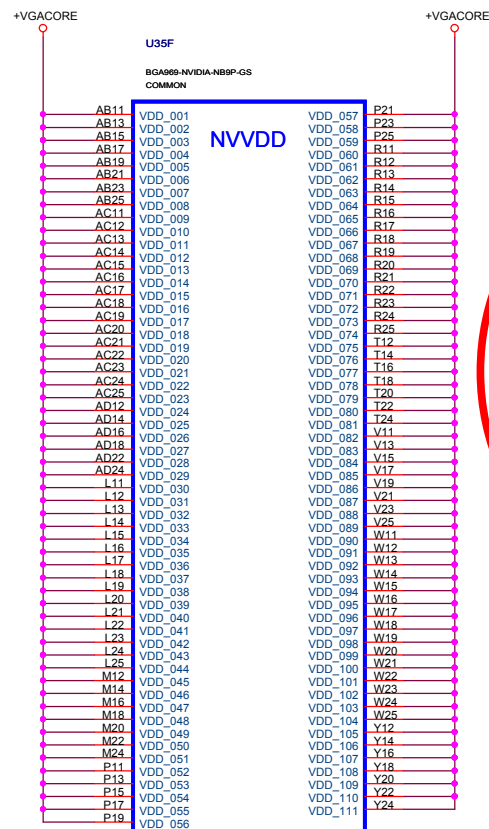
NB9M-GE	0x06E	8	1000 default
NB9M-GS	0x06E	9	1001
NB9P-GE2	0x064	8	1000
NB9P-GS	0x064	9	1001 default

NB9X VRAM Configuration Table

RAM_CFG[3:0]	DESCRIPTION	Vendor
0111	DDR2 32Mx16x8, 128bit, 512MB	Hynix HY8PS121621CFF-26
0110	DDR2 32Mx16x8, 128bit, 512MB	Qimonda HYB18T512161BFZ-26
0101	DDR2 32Mx16x8, 128bit, 512MB	K4N51163QC-ZC25 Nanya/Epidia
0000	DDR2 64Mx16x8, 128bit, 1GB	Hynix
0001	DDR2 64Mx16x8, 128bit, 1GB	Samsung
0010	DDR2 64Mx16x8, 128bit, 1GB	Qimonda

CS33572FB13 RES CHIP 35.7K 1/16W +-1% (0402)

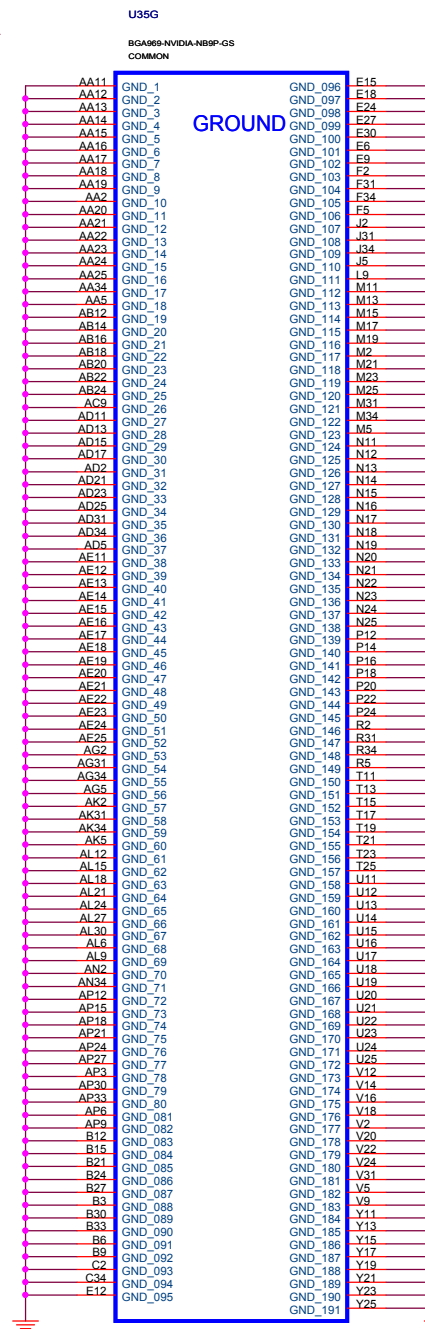
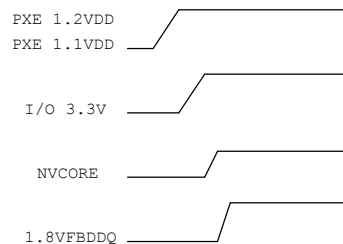
NVVDD Decoupling

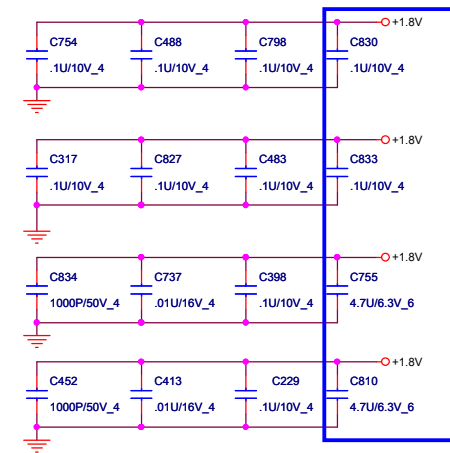
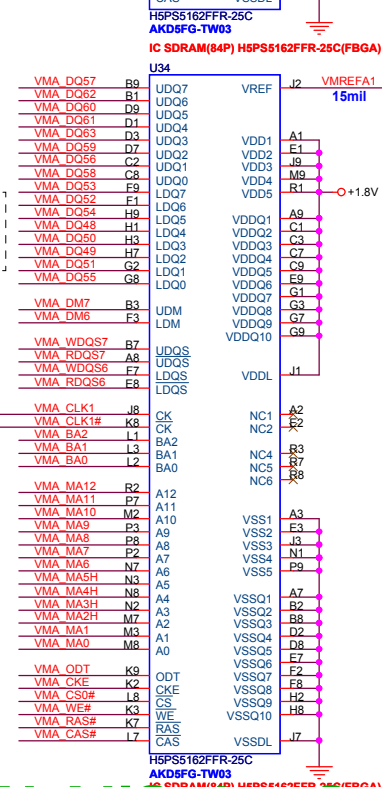
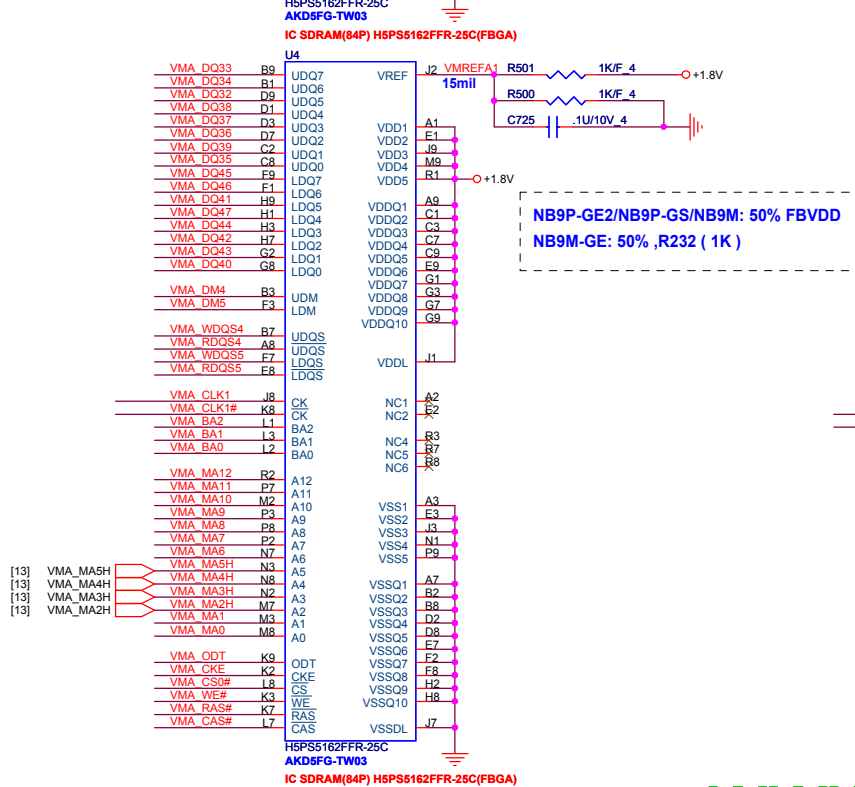
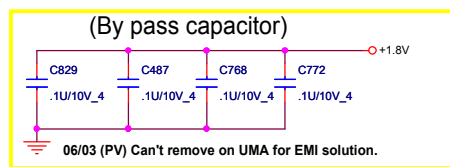
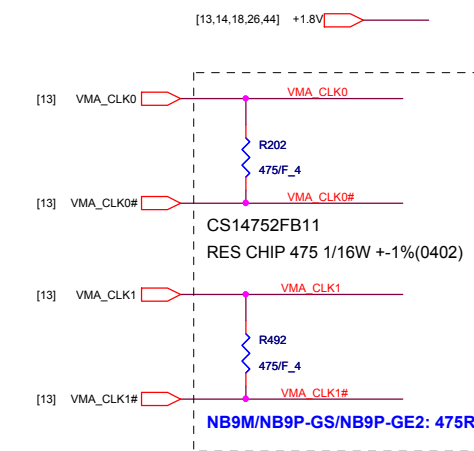
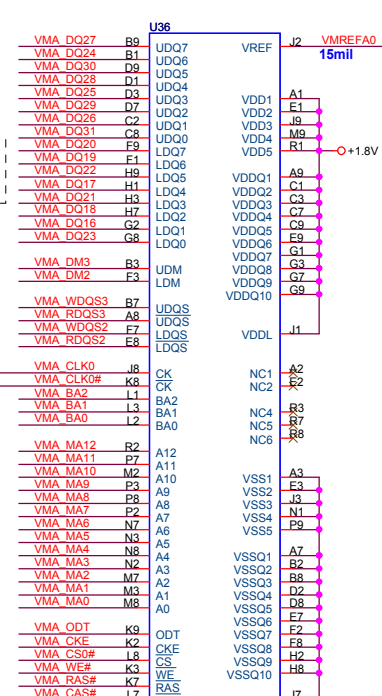
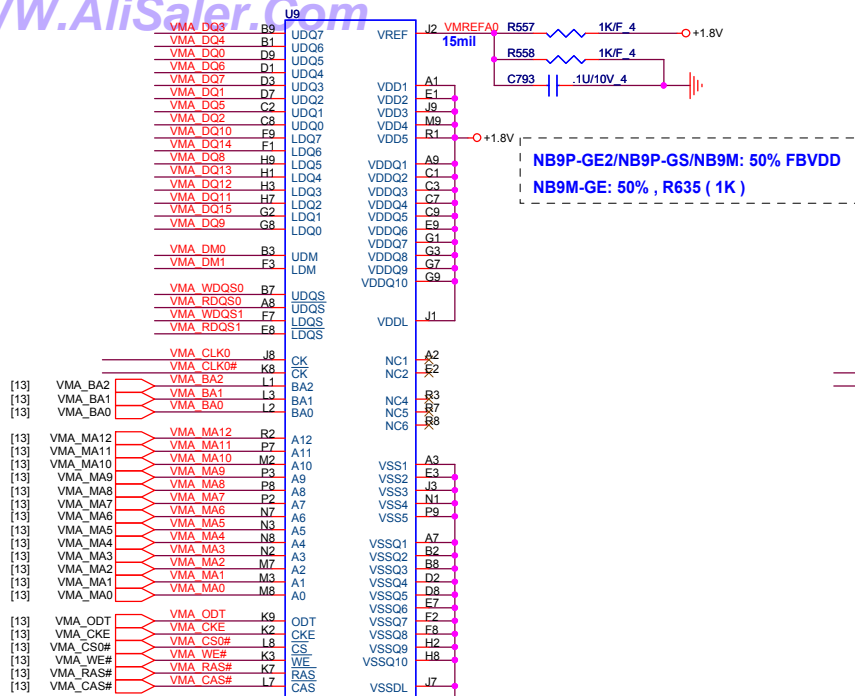


Follow Design Guide DG-03276-001 4.7uF x3 and 0.47x10 uF instead of 0.1uF x10

NB9M: VGACORE +0.90V (Normal) , +1.09V

power up sequence

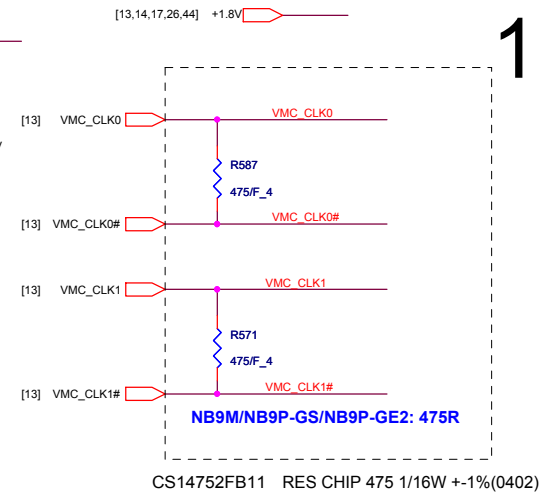
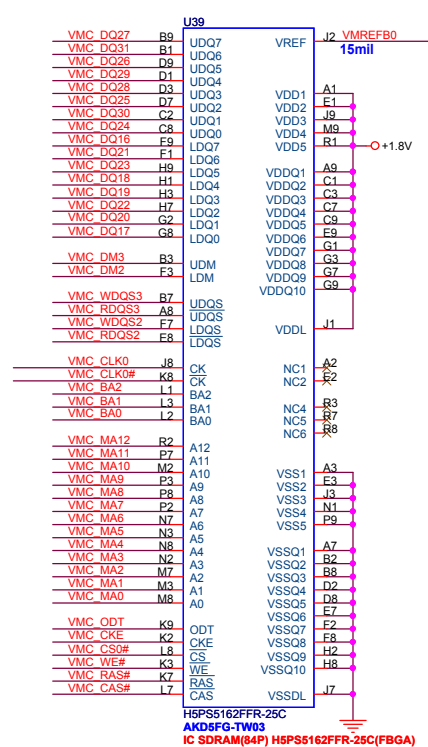




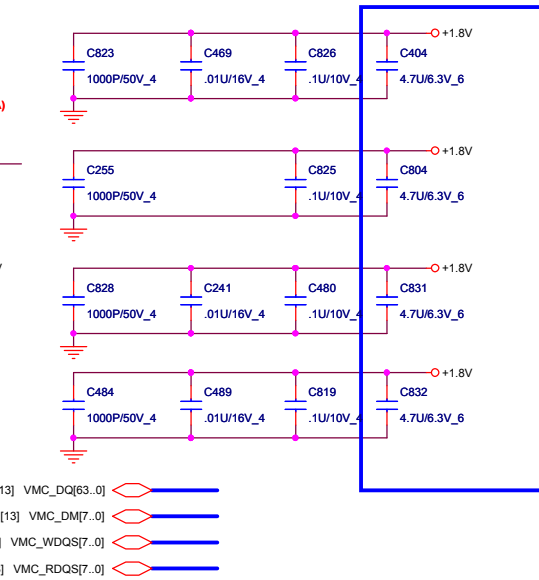
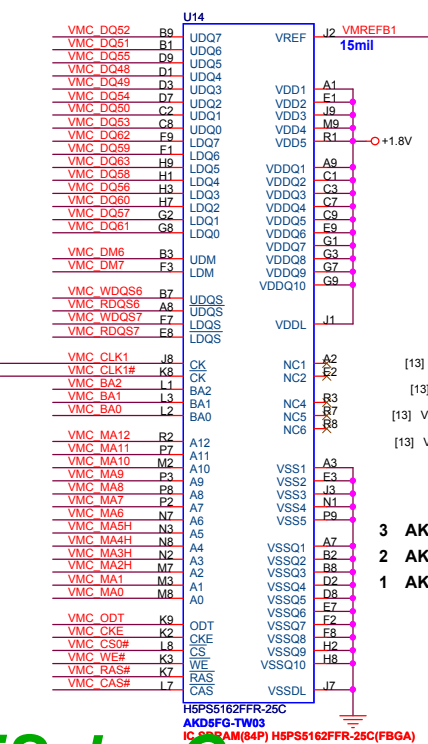
For DB:
NB9P : AKD59G-T502(Samsung,32M*16)
NB9M : AKD5FG-TW31(Hynix,32M*16)
AKD5FG-T*03(Qimonda 32M*16)

PROJECT : UT7
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Size Custom	Document Number	Rev E3A
	NV9X VRAM-1(GDDR2 BGA84)	
Date: Friday, July 18, 2008	Sheet 17 of 46	



CS14752FB11 RES CHIP 475 1/16W +-1%(0402)



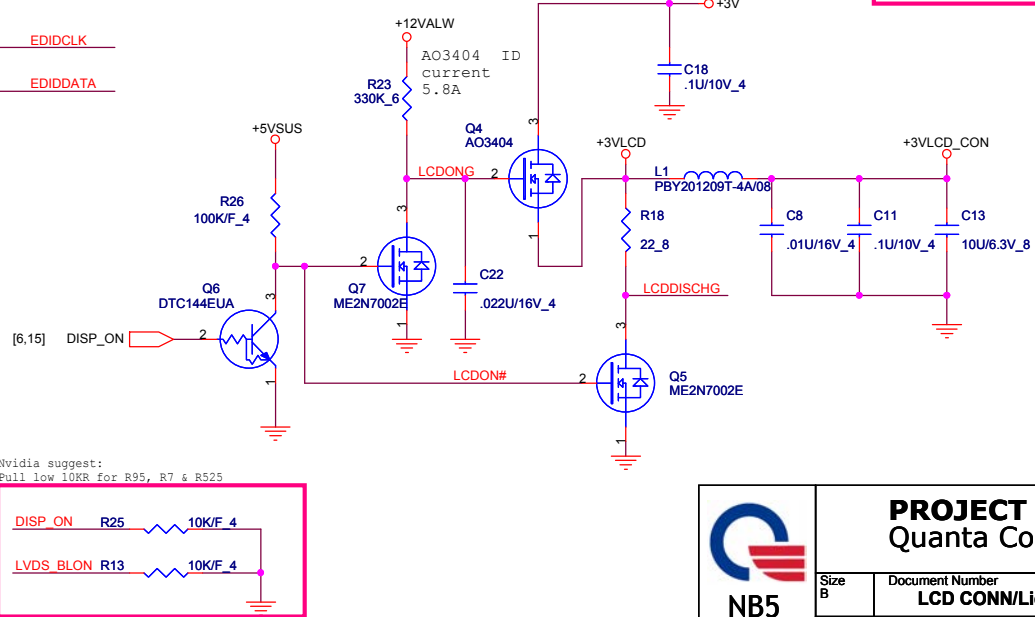
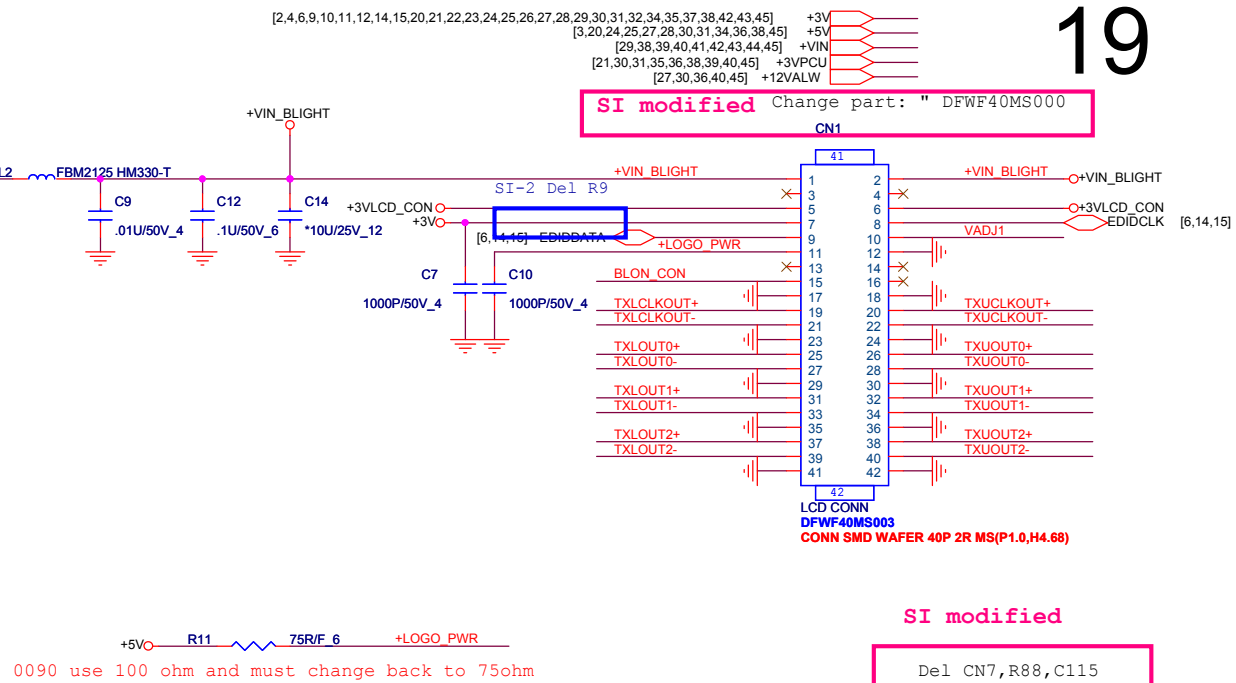
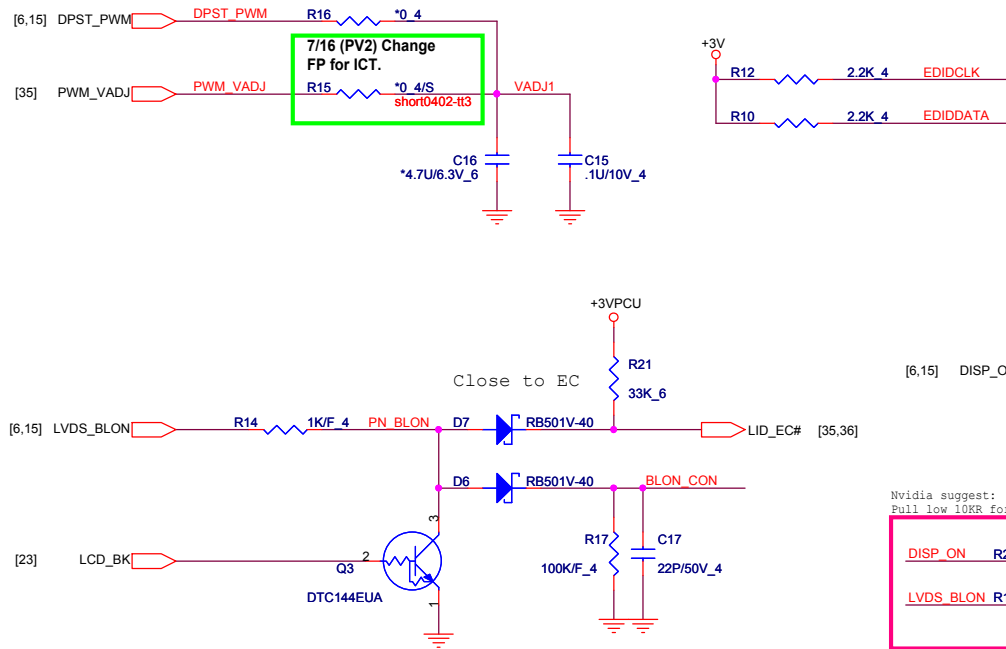
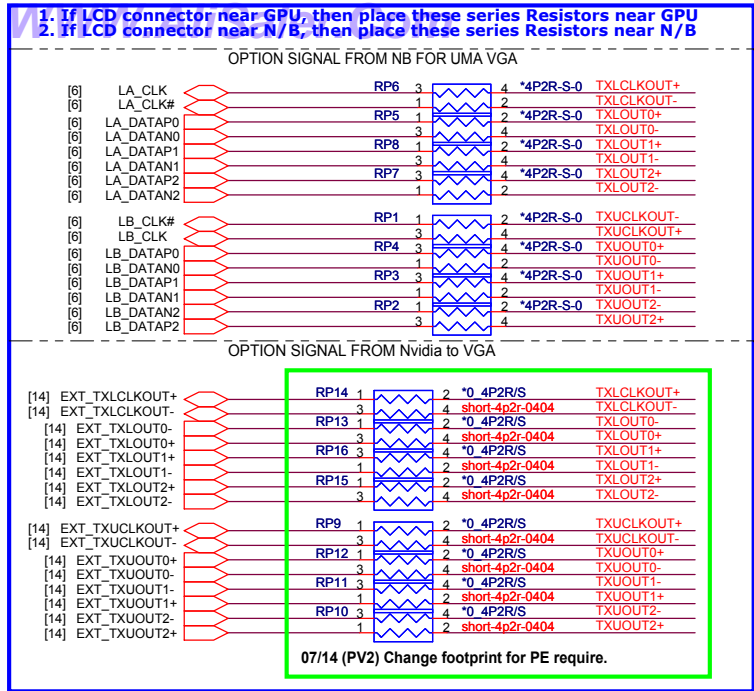
	VRAM	Vendor
NB9M-GE	1	3
NB9P-GS	2	

- | | | | |
|---|-------------|--|---------|
| 3 | AKD5FG-T501 | IC SDRAM(84P) K4N51163QG-HC25(FBGA) | Samsung |
| 2 | AKD5FG-T^03 | IC SDRAM(84P)HYB18T512161B2F-25(TFBGA) | Qimonda |
| 1 | AKD5FG-TW31 | IC SDRAM(84P) HY5PS121621CFP-25(FBGA) | Hynix |



PROJECT : UT7
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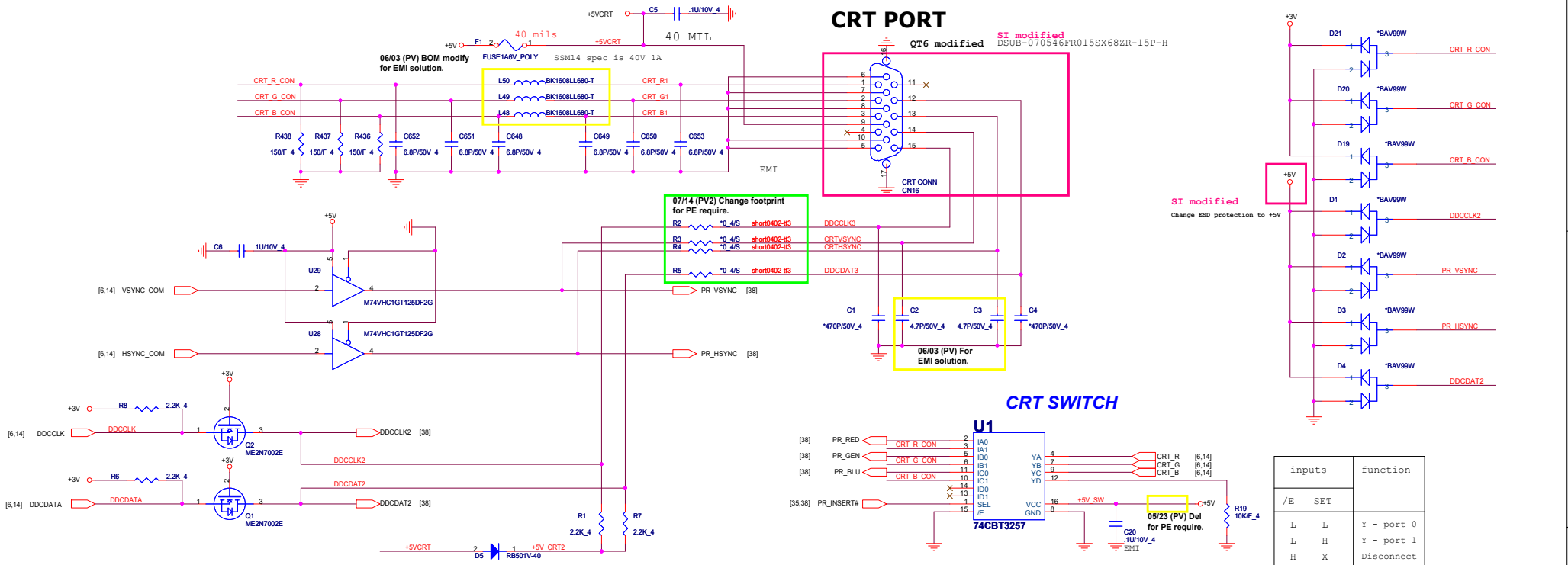
Size Custom	Document Number NV9X VRAM-2(GDDR2 BGA84)	Rev E3A
Date: Friday, July 18, 2008	Sheet 18 of 46	



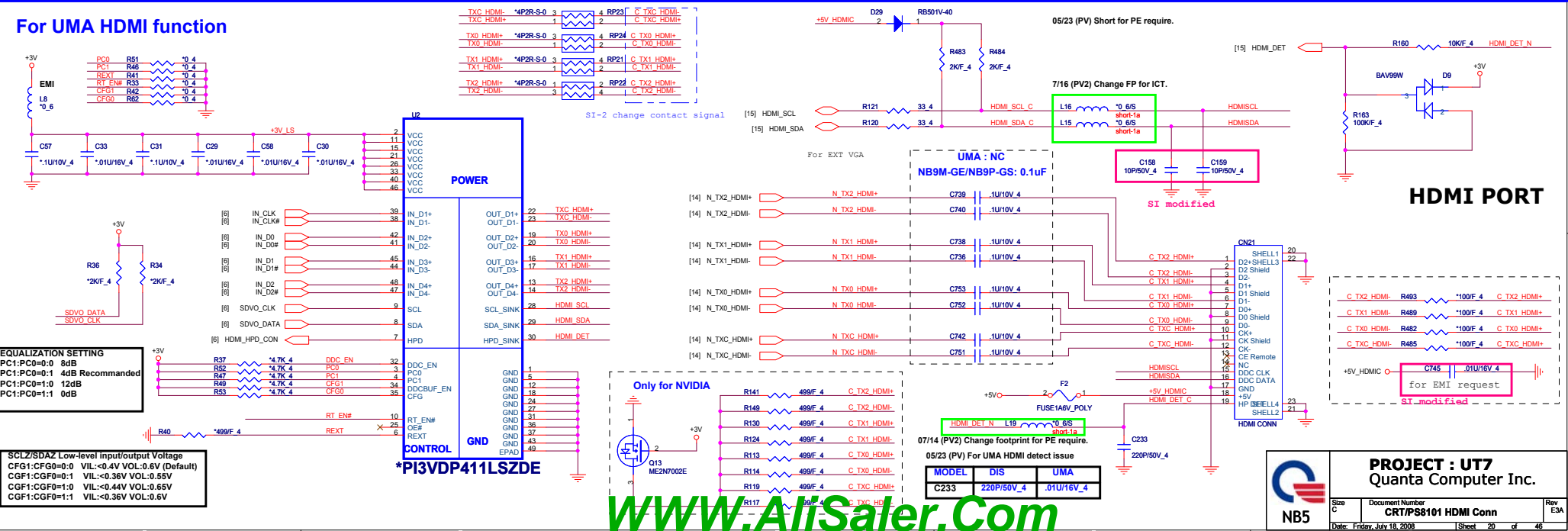
PROJECT : UT7
Quanta Computer Inc.

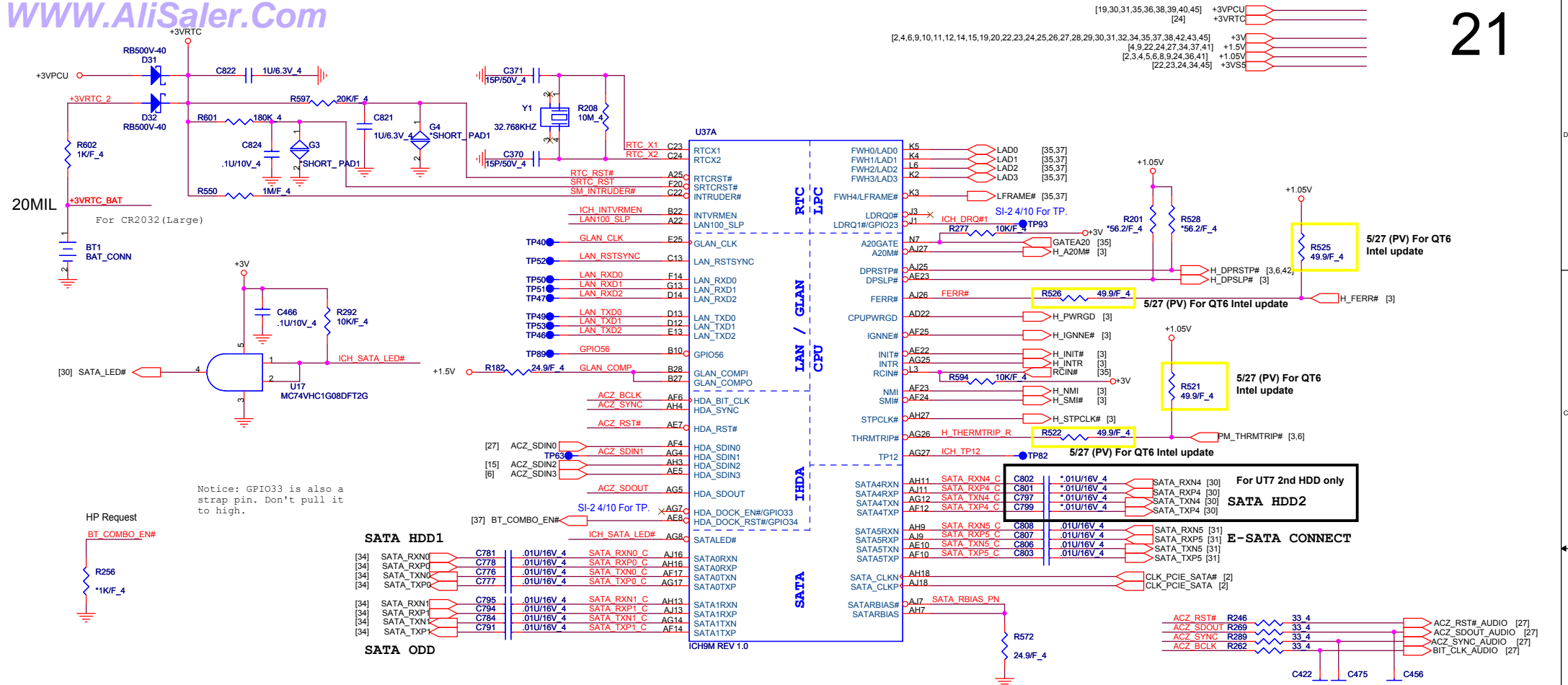
NB5

Size B	Document Number	Rev E3A
LCD CONN/Lid function		
Date: Friday, July 18, 2008	Sheet 19 of 46	



For UMA HDMI function





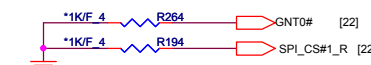
SB Strap

ICH9-M Internal VR Enable strap (Internal VR for VccSus1_05, VccSus1_5 and VccCL1_5)	ICH9-M LAN100_SLP Strap (Internal VR for VccLAN1_05 and VccCL1_05)
INTVRMEN Low = Internal VR disable High = Internal VR enable(Default)	LAN100_SLP Low = Internal VR disable High = Internal VR enable(Default)

XOR Chain Entrance Strap		
ICH_TP3	HDA_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal operation(Default)
1	1	Set PCIe port config bit 1

ICH9 Boot BIOS select		
STRAP	PCI_GNT0#	SPI_CS#1
SPI	0	1
PCI	1	0
LPC	1	1

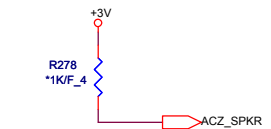
(default)



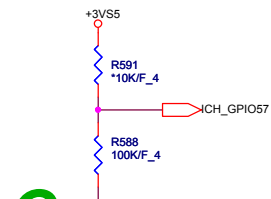
A16 swap override strap	
PCI_GNT#3	Low = A16 swap override enabled Hi = Default



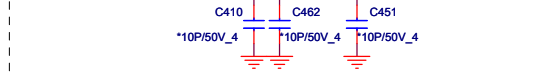
No Reboot Strap	
ACZ_SPKR	Low: Default Hi: No reboot



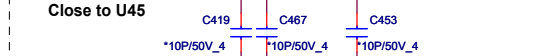
TPM physical presence	
ICH_GPIO57	Low: Default



For GM UMA only		
ACZ_RST# R242	*33 4	ACZ_RST#_MCH [6]
ACZ_SDOUT R266	*33 4	ACZ_SDOUT_MCH [6]
ACZ_SYNC R274	*33 4	ACZ_SYNC_AUDIO [27]
ACZ_BCLK R258	*33 4	ACZ_BITCLK_MCH [6]



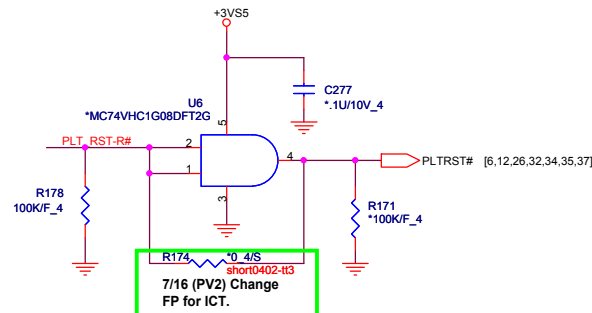
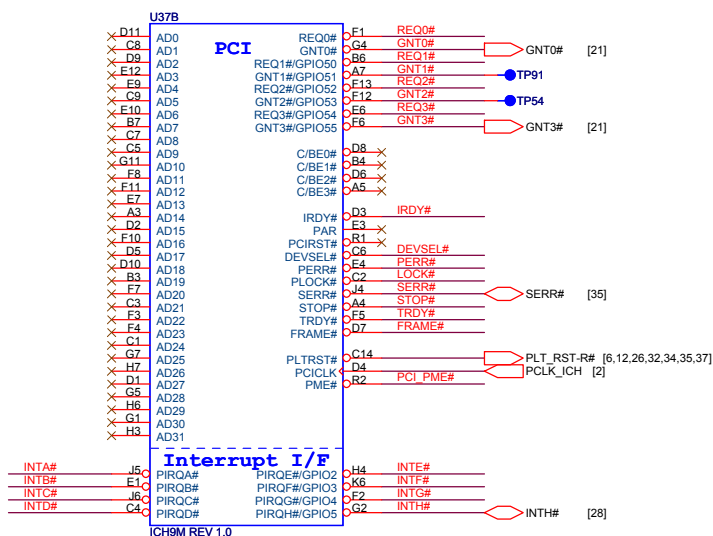
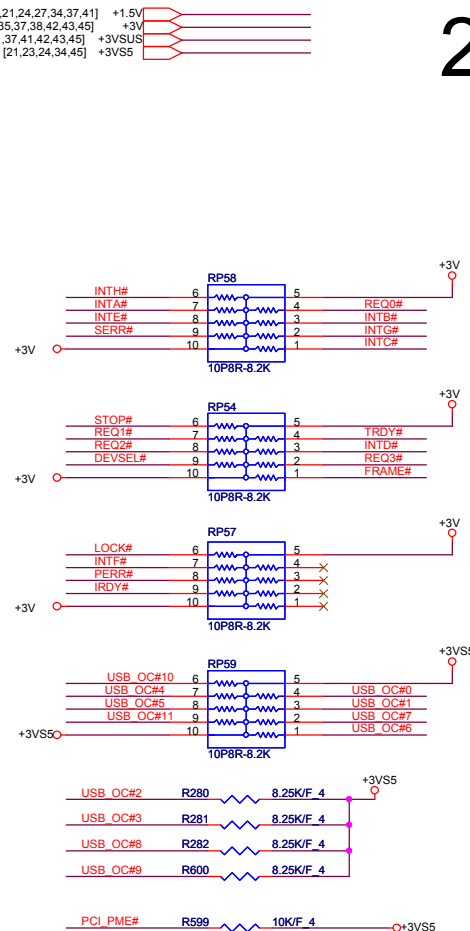
ACZ_RST# R252	22 4	NV_HDA_RST [15]
ACZ_SDOUT R268	22 4	NV_HDA_SDO [15]
ACZ_SYNC R263	22 4	NV_HDA_SYNC [15]
ACZ_BCLK R261	22 4	NV_HDA_BCLK [15]

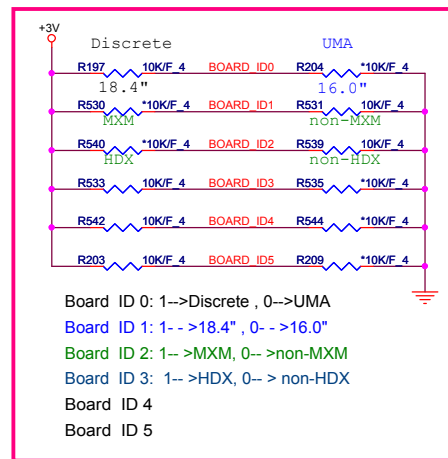


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Size Custom Document Number ICH9-M Host 1/4 Rev E3A

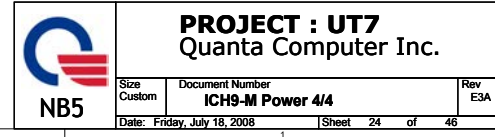
Date: Friday, July 18, 2008 Sheet 21 of 46



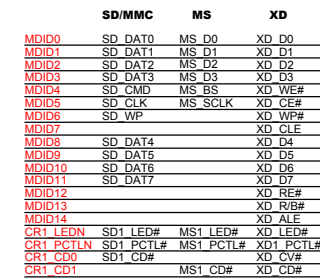
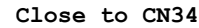


PROJECT : UT7
Quanta Computer Inc.

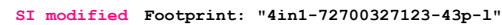
Size Custom	Document Number ICH9-M GPIO 3/4	Rev E3A
Date: Friday, July 18, 2008		Sheet 23 of 46



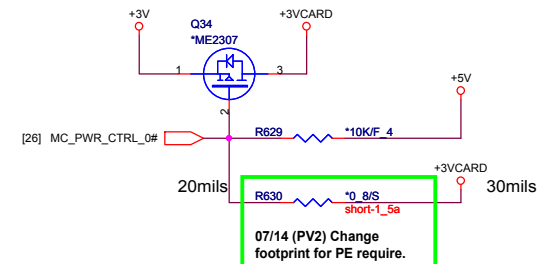
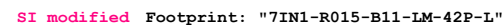
25

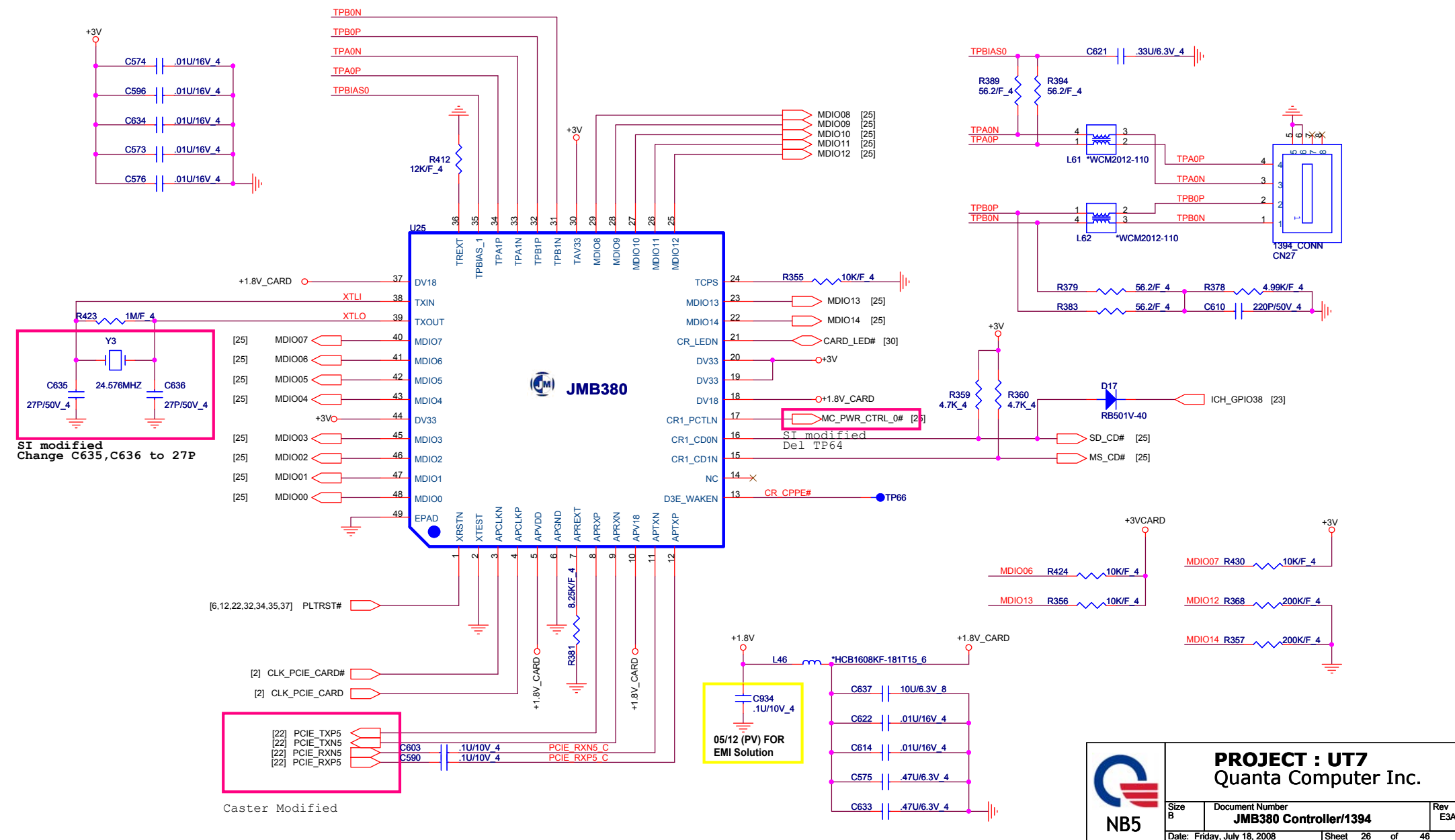


XD,MMC/SD,MS/MSP




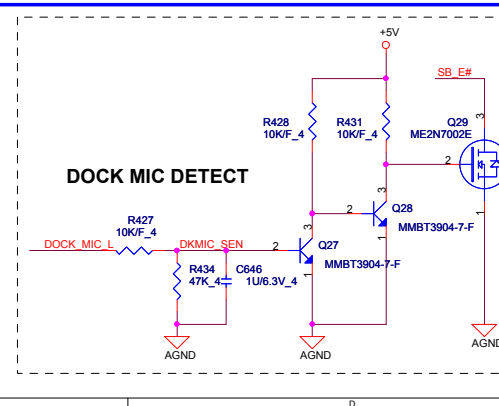
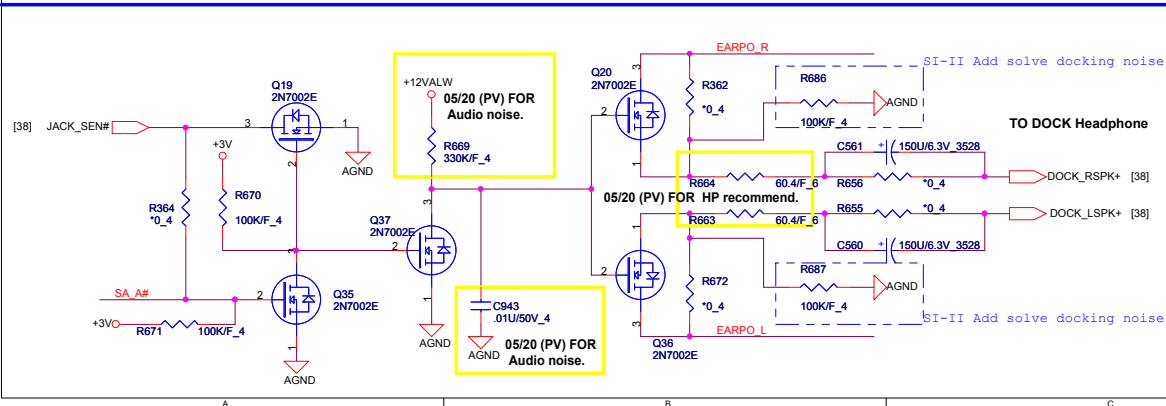
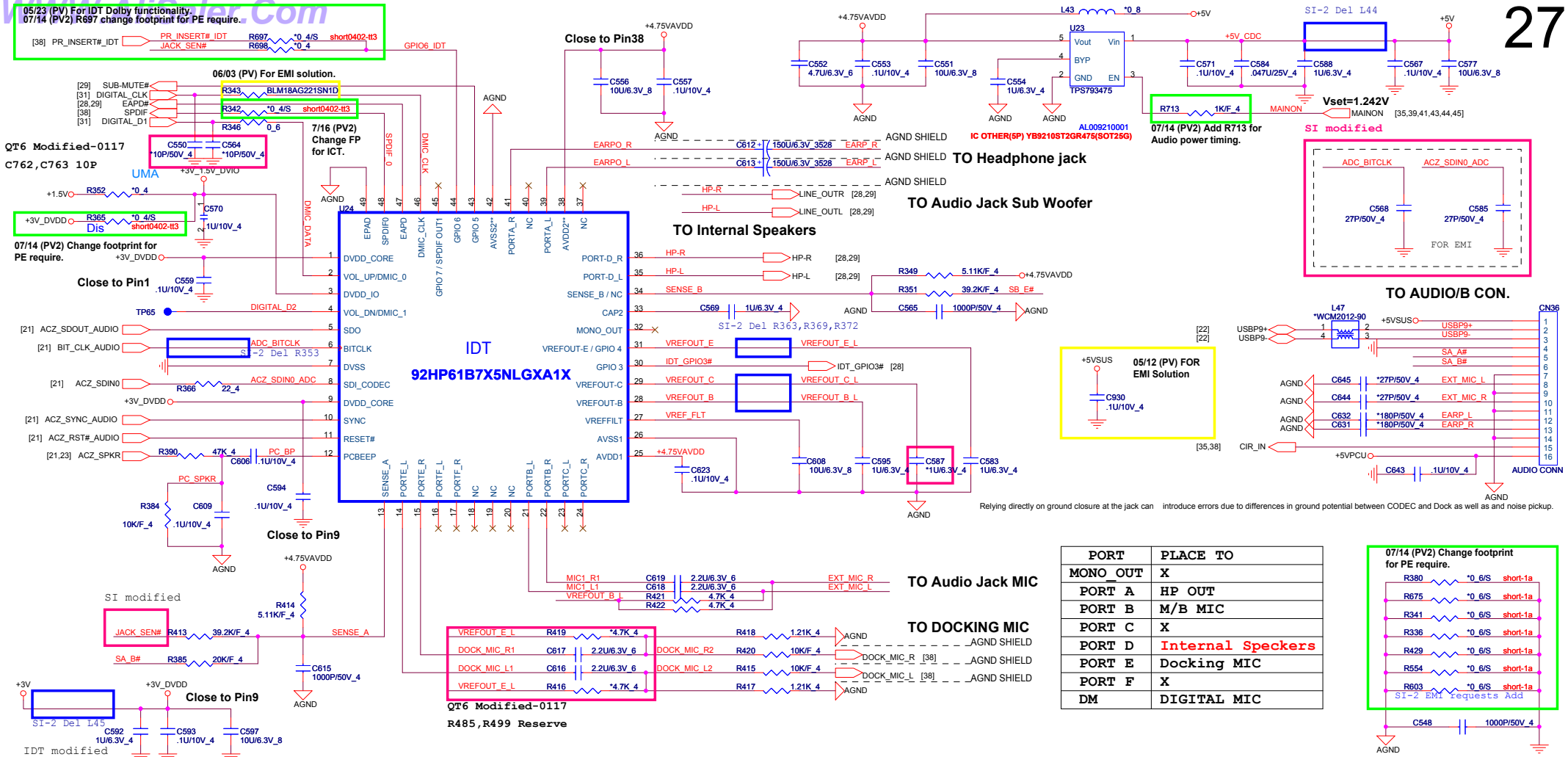
2ND SOURCE






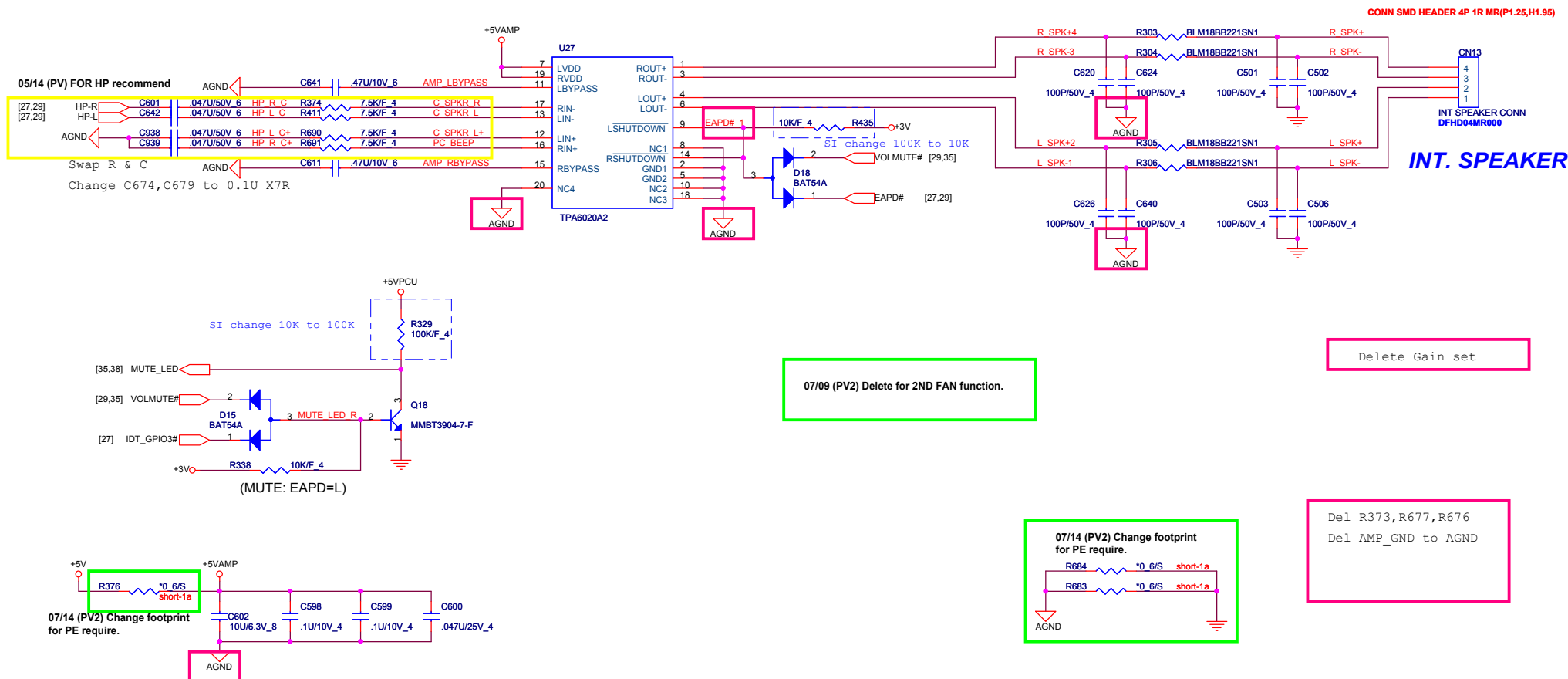
05/23 (PV) For IDT Dolby functionality.
07/14 (PV2) R697 change footprint for PE require.

[38] PR_INSERT#_IDT  PR_INSERT#_IDT R697 *0 4/S short0402-tt3
JACK_SEN# R698 *0 4

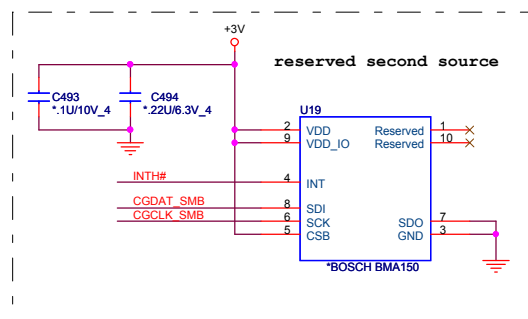
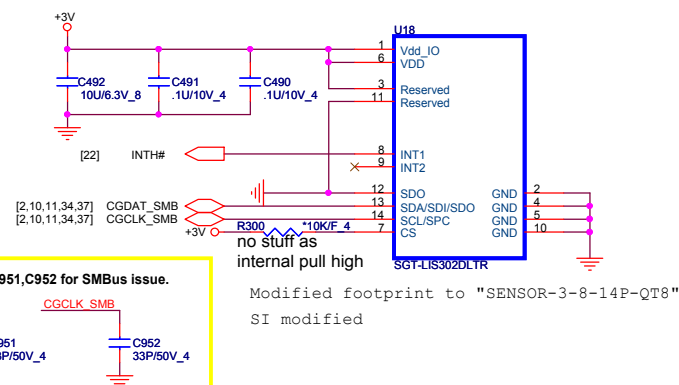


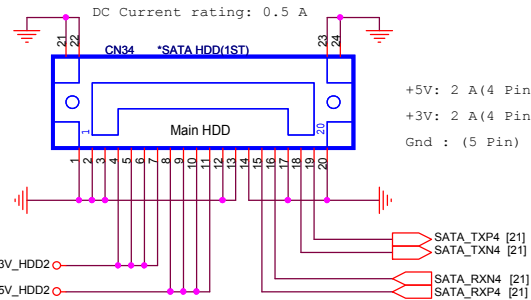
```
SA_A# -->EXT HP
SA_B# -->EXT MIC
SB_E#--> DOCK MIC
Audio JACK: Normal Open
```

 NB5	PROJECT : UT7 Quanta Computer Inc.		
	Size Custom	Document Number Azalia IDT92HD71B7	Rev E3
Date: Friday, July 18, 2008		Sheet 27 of 46	

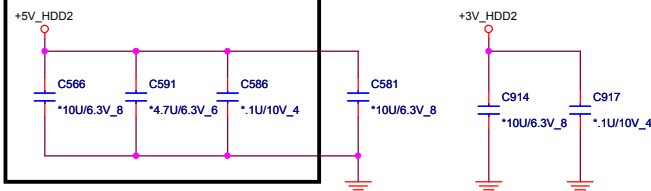


Accelerometer Sensor



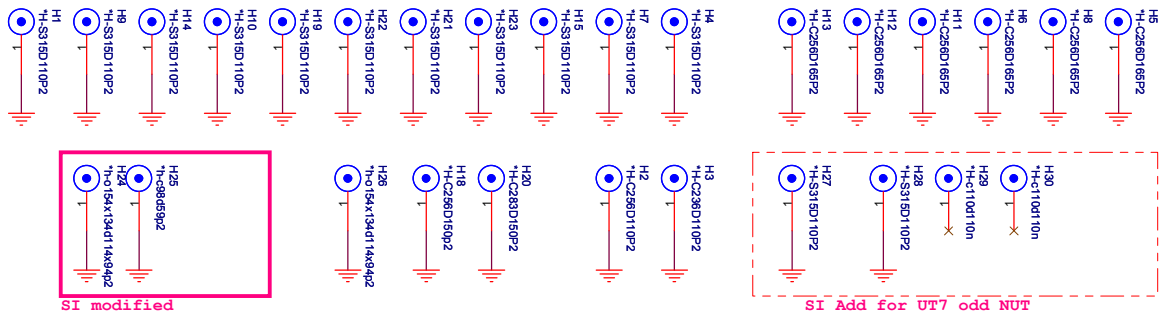


FOR UT7 2ND HDD ONLY.



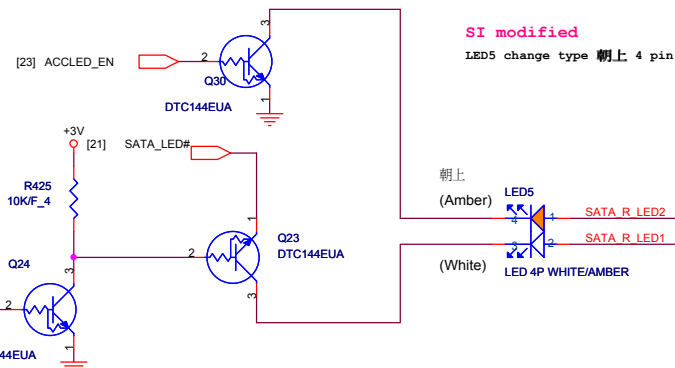
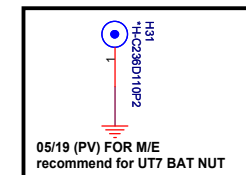
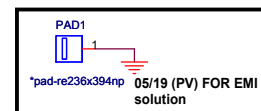
+3VPCU_LED [36]
 +3V_LED [36]
 +5V [3, 19, 20, 24, 25, 27, 28, 31, 34, 36, 38, 45]
 +3V [2, 4, 6, 9, 10, 11, 12, 14, 15, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 31, 32, 34, 35, 37, 38, 42, 43, 45]
 +12VALW [19, 27, 36, 40, 45]

M/B Screw Hole

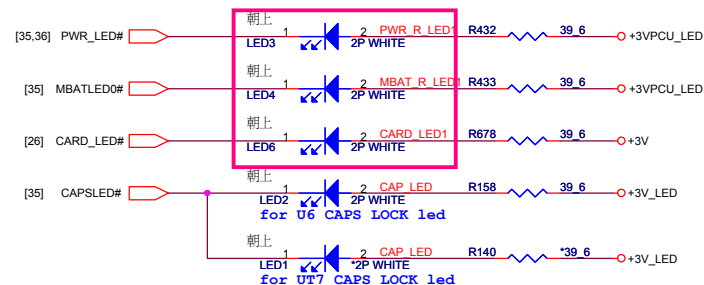


delete all PAD & change screw footprint

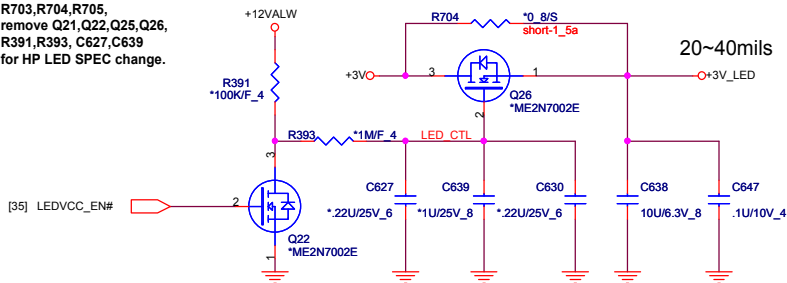
07/14 (PV2) Delete H16,H17 for no support ROBSON card.



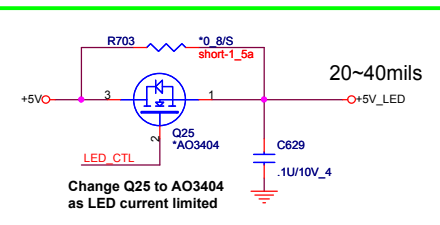
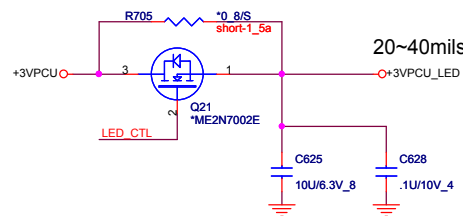
SI modified LED3,4,6 change type 朝上 2 pin



07/09 (PV2) Add R703,R704,R705, remove Q21,Q22,Q25,Q26, R391,R393, C627,C639 for HP LED SPEC change.

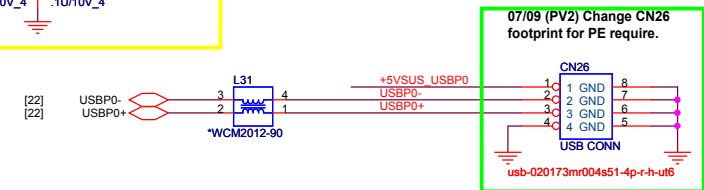
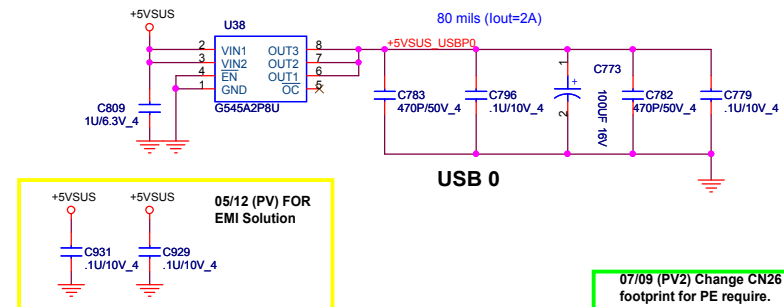
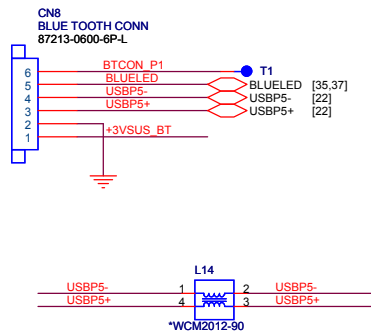
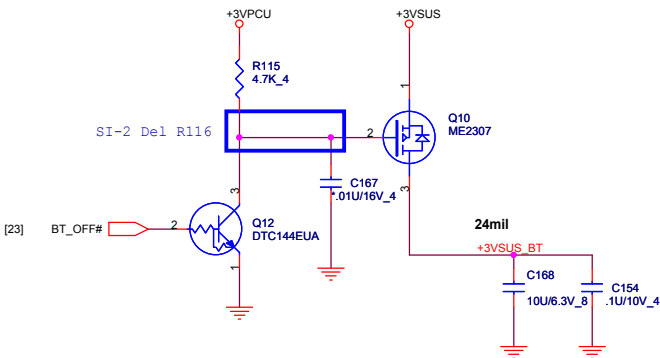


LED PWR CONTROL

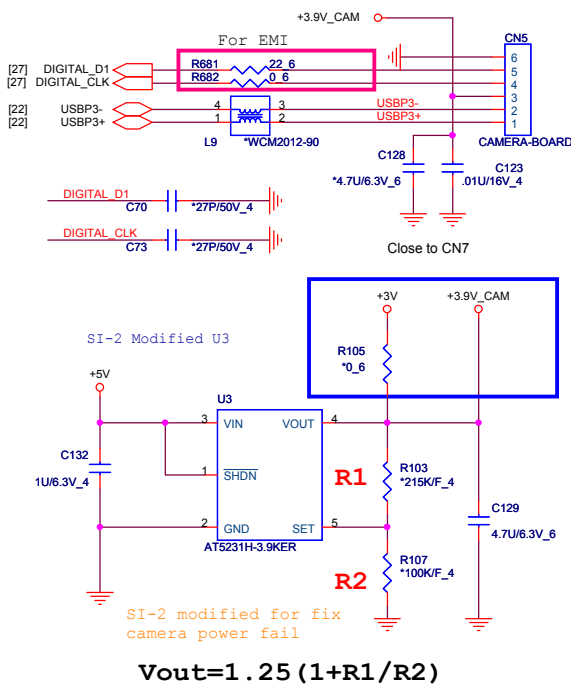


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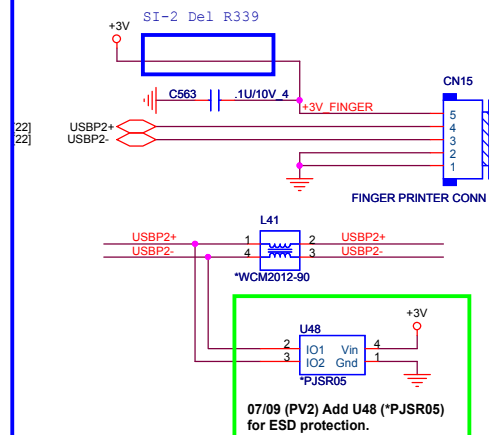
Size Custom	Document Number	Rev E3A
	LED & 2nd HDD & Hole	
Date: Friday, July 18, 2008	Sheet 30 of 46	



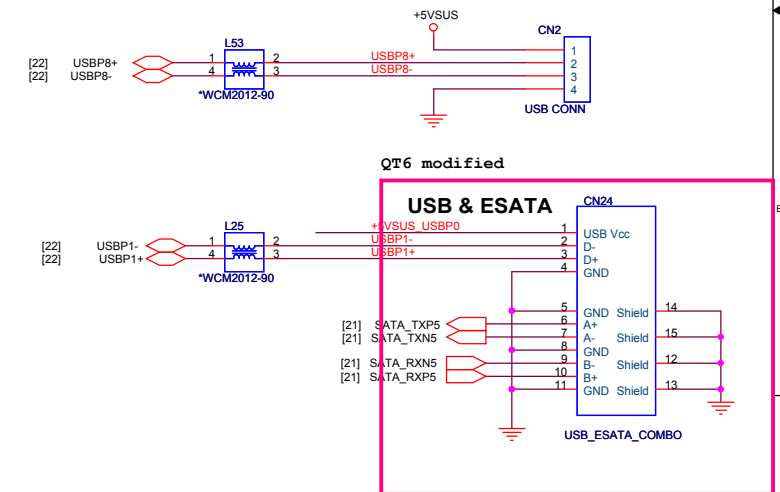
USB CAMERA /DIGITAL MIC CONNECT

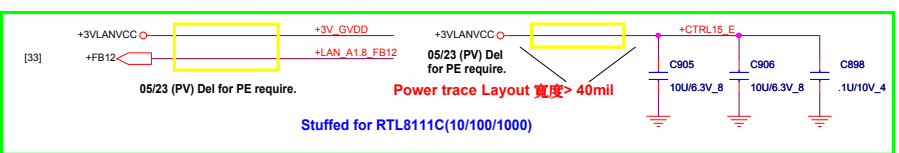
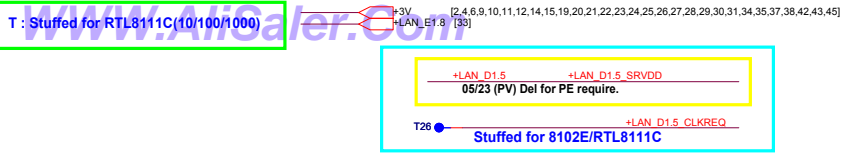


USB fingerprint CON

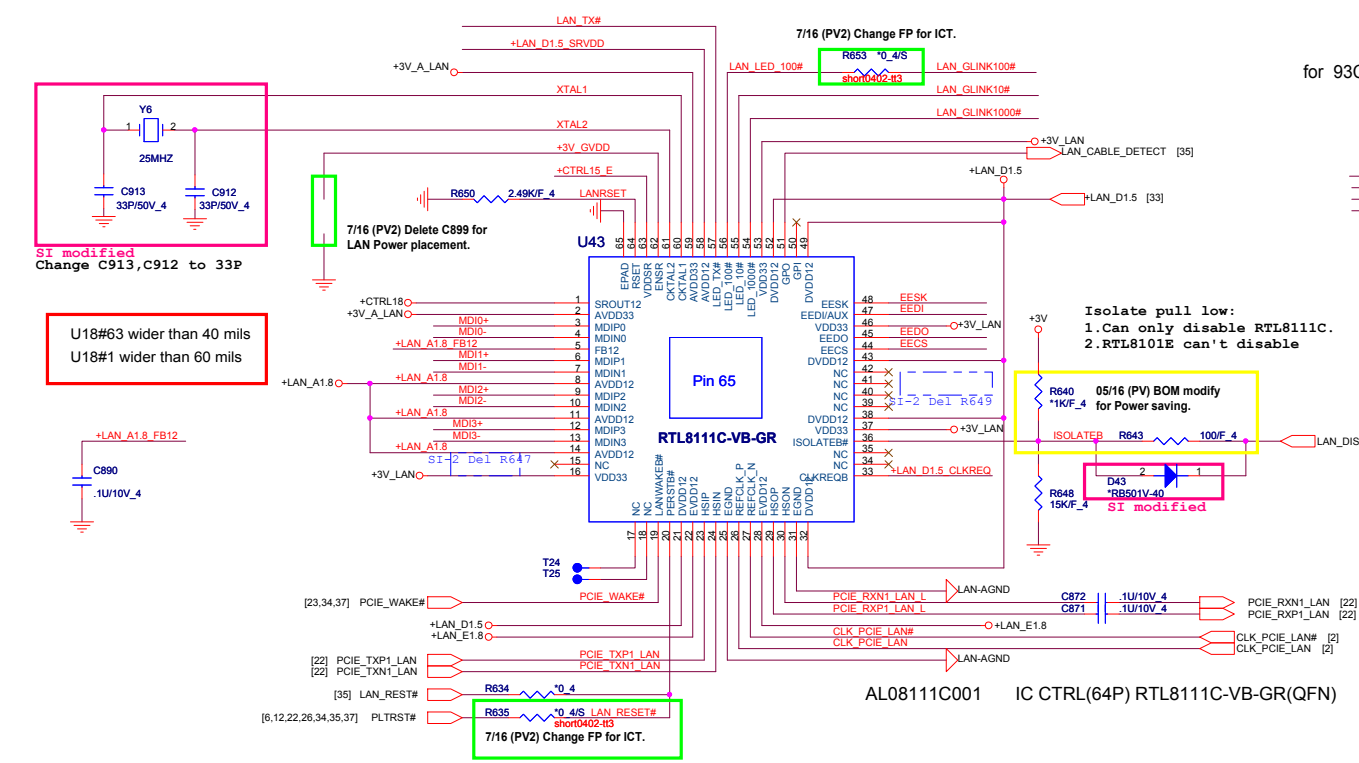


1. ESD GND
2. SYSTEM GND
3. USB-
4. USB+
5. USB PWR(+3V)

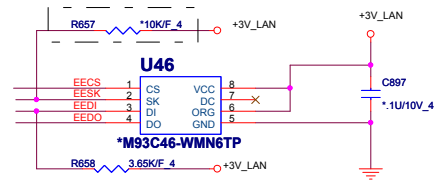




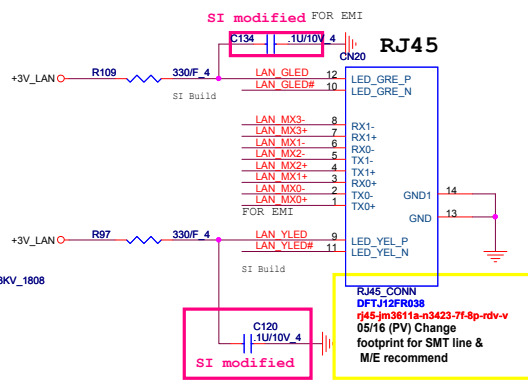
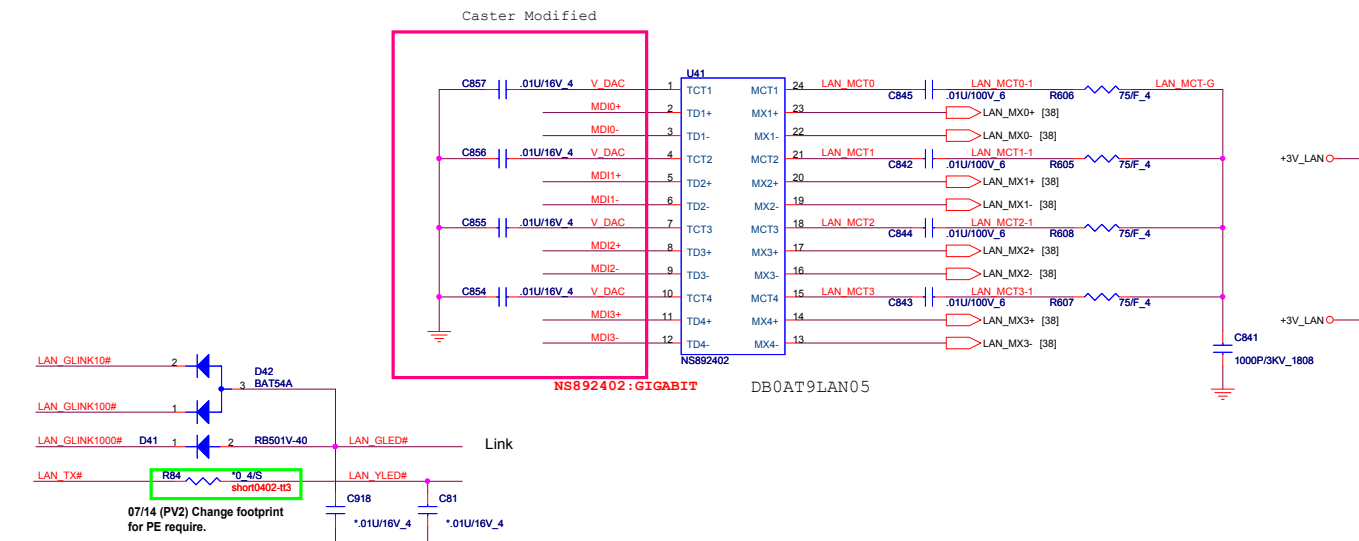
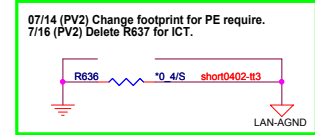
32

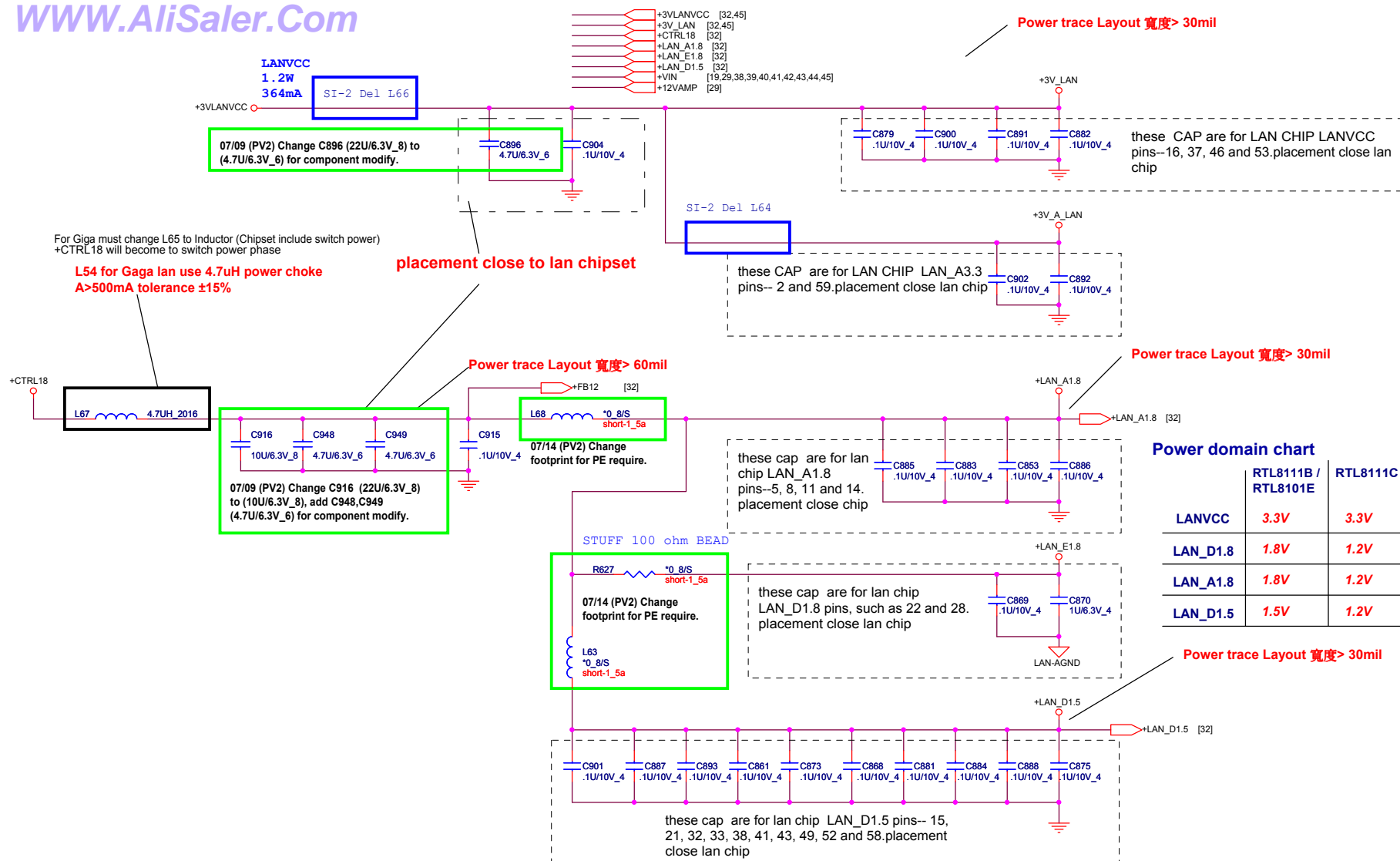


for 93C56 used. NC if 93C46 is used.

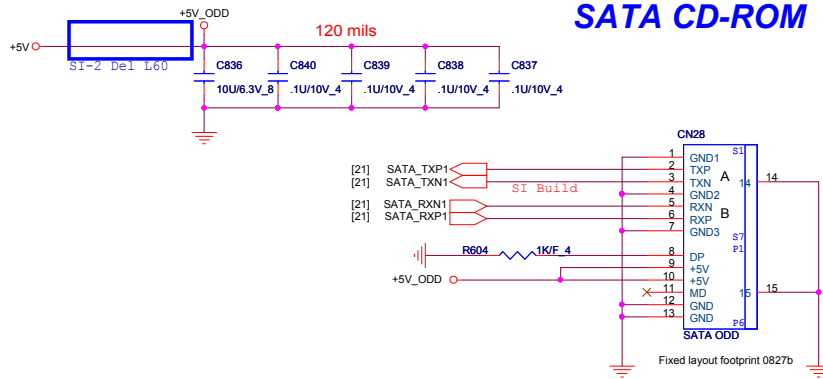


if ISOLATE pin pull-low, the LAN chip will not drive it's PCI-E outputs (excluding PCIE_WAKE# pin)

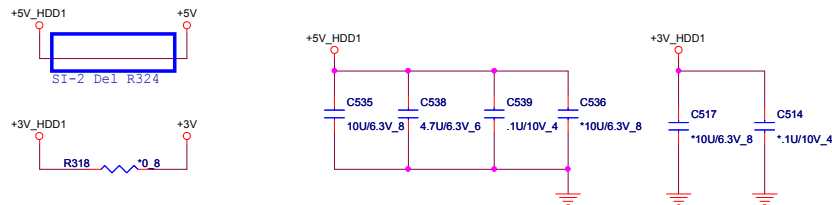
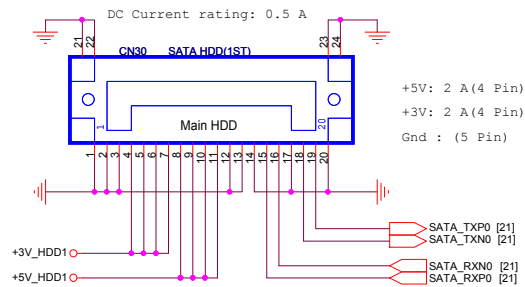




SATA CD-ROM



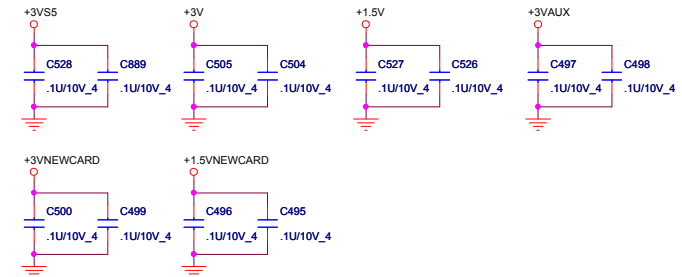
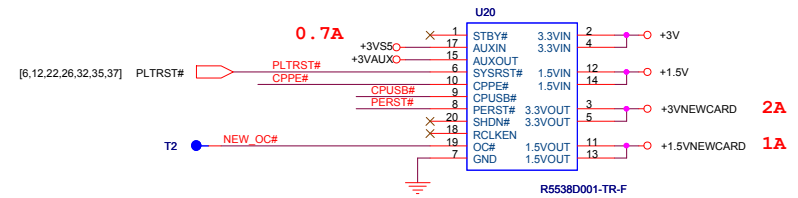
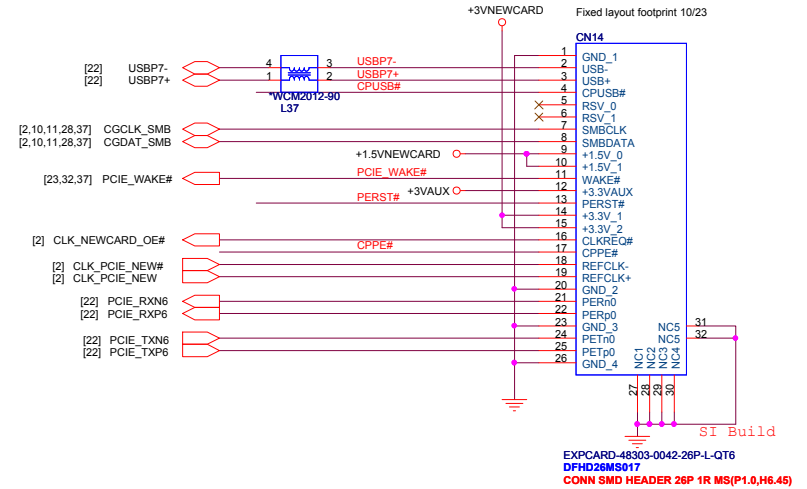
SATA_1 CONNECTOR




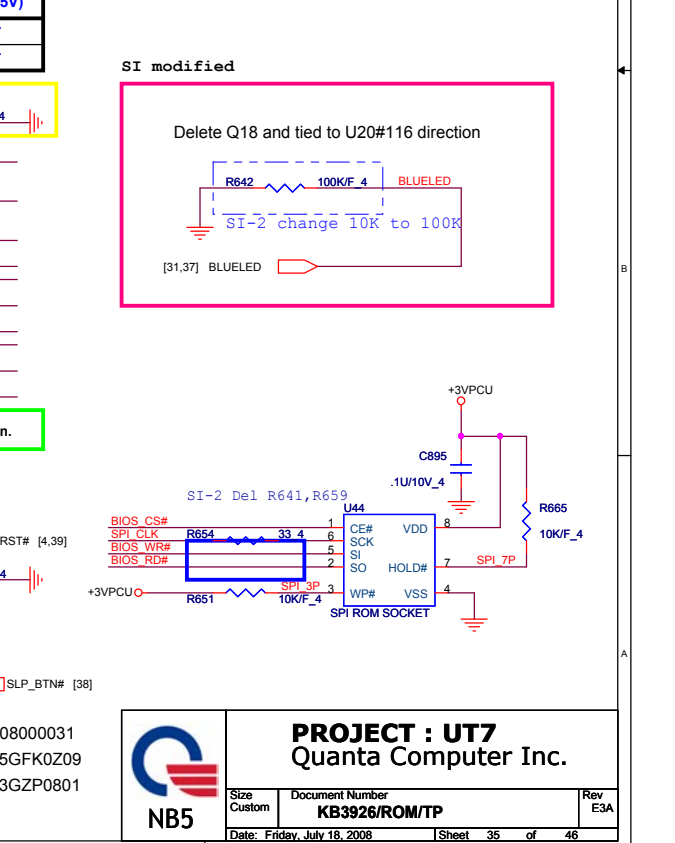
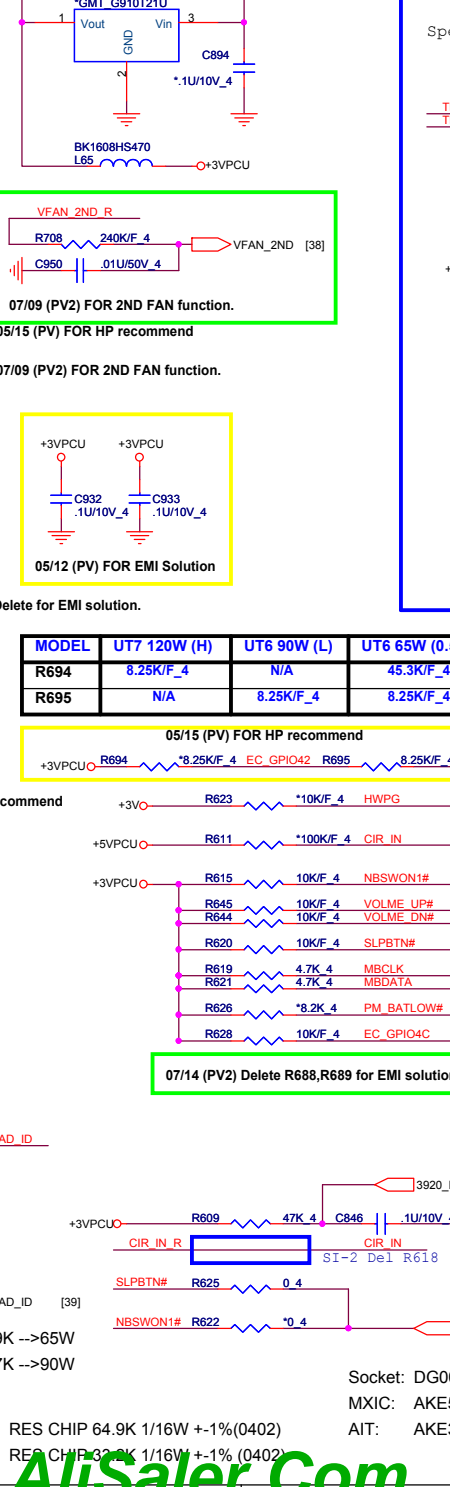
NEWCARD

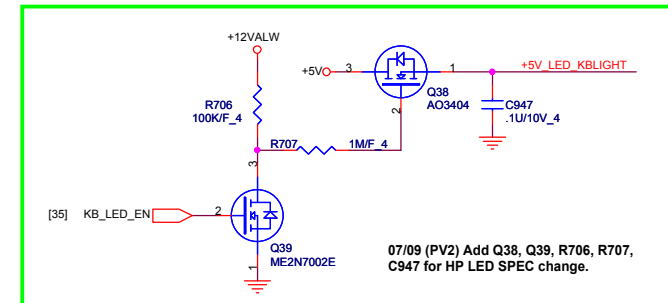
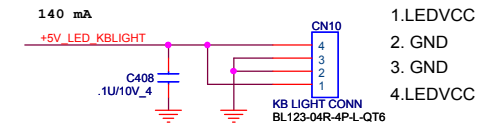
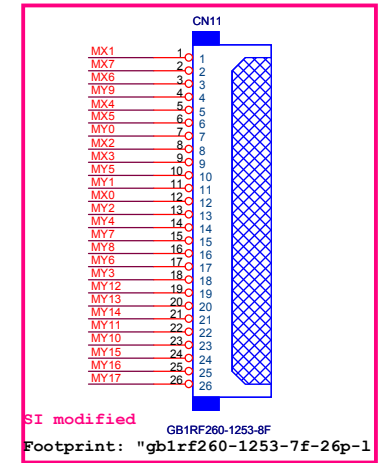
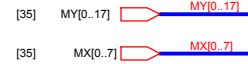
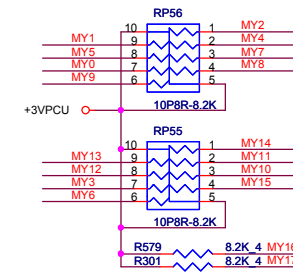
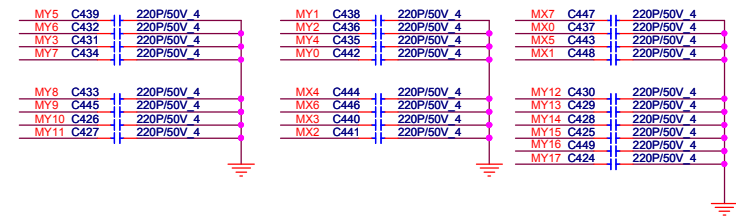
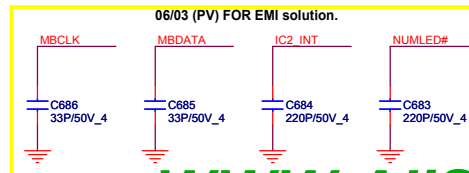
NEWCARD (PCIEXPRESS*1 + USB*1)

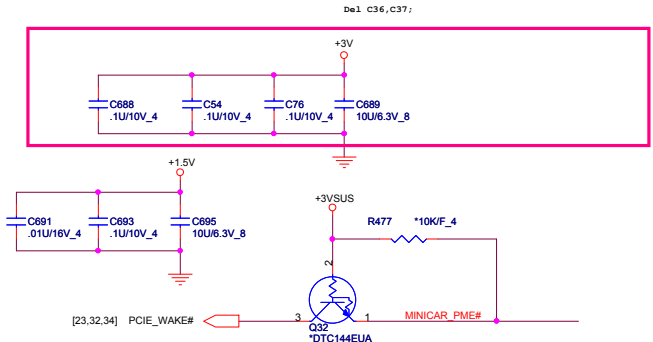
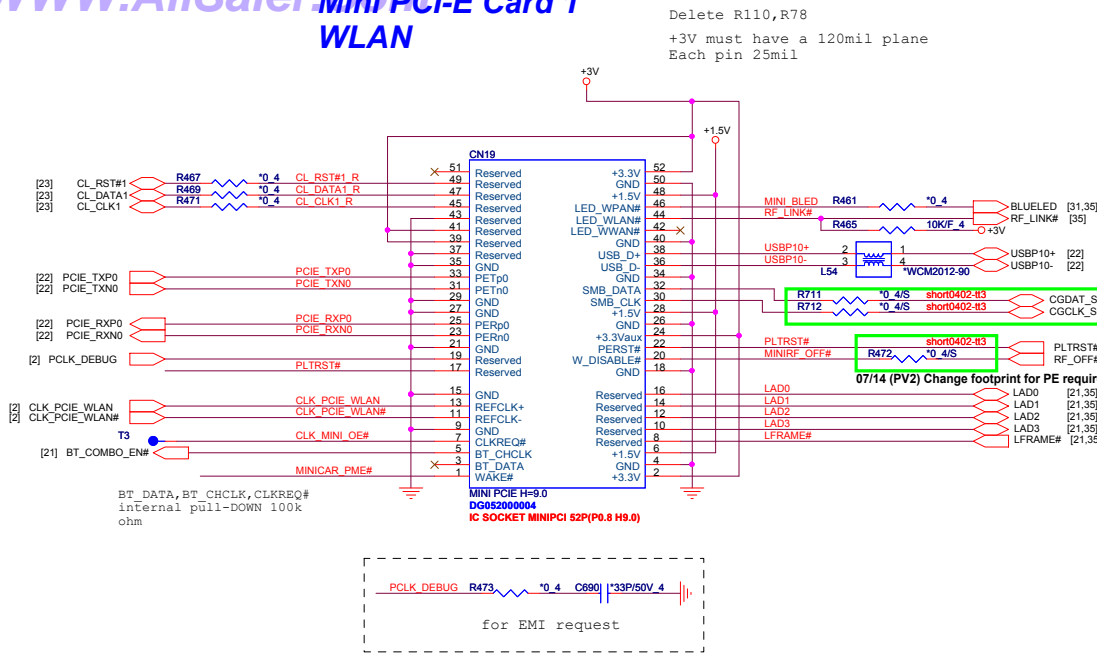
34



	PROJECT : UT7 Quanta Computer Inc.		
	Size Custom Document Number ODD/HDD/NEW CARD	Date: Friday, July 18, 2008	Rev E3A



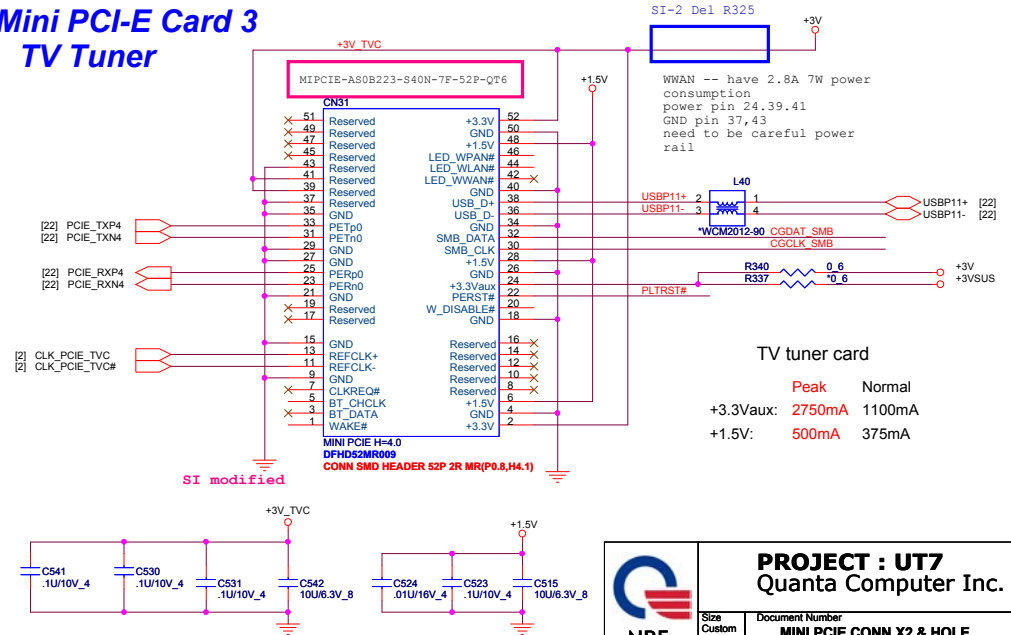


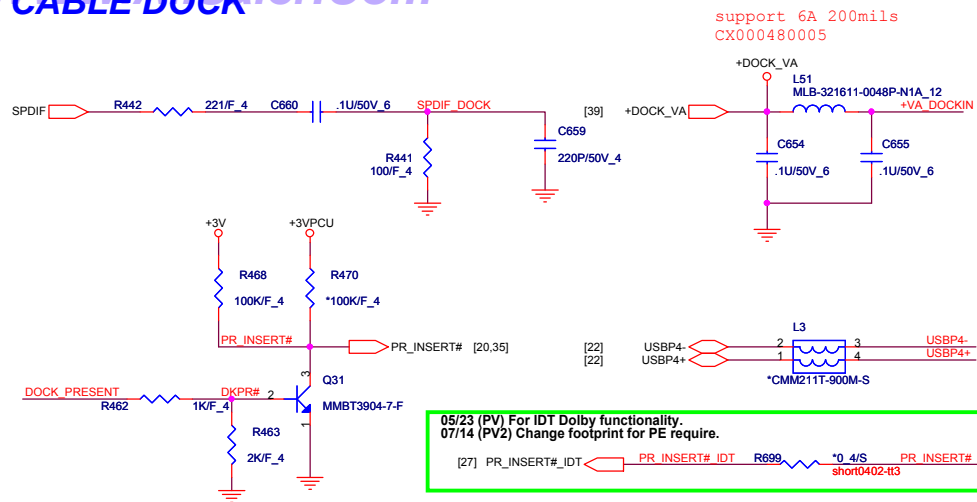


Mini PCI-E Card 2 ROBSON

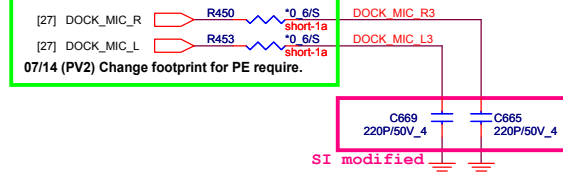
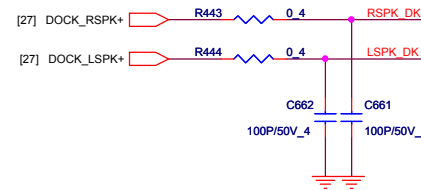
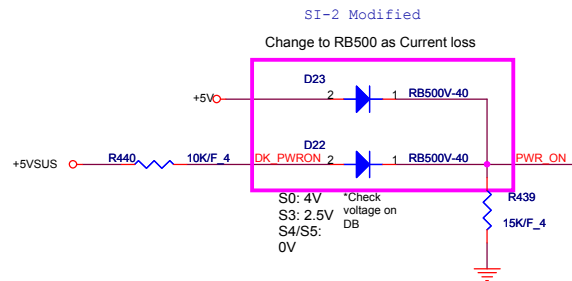
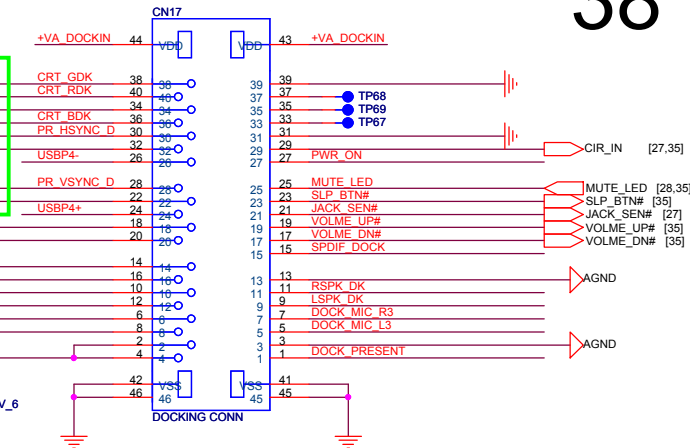
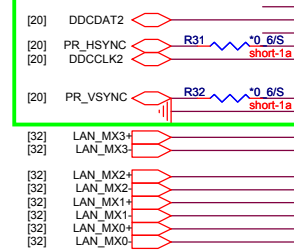
07/09 (PV2) Delete for no support ROBSON card.

Mini PCI-E Card 3 TV Tuner



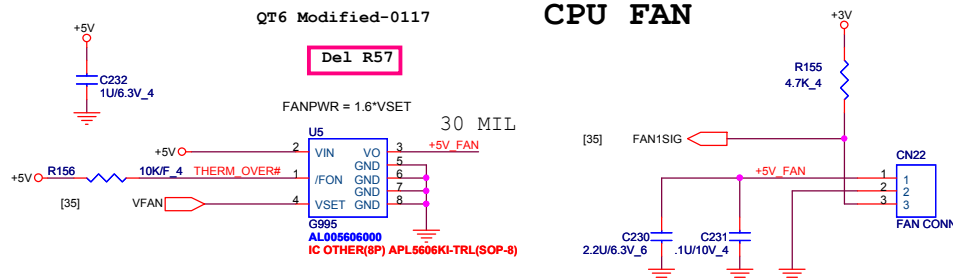


07/14 (PV2) Change footprint for PE require.

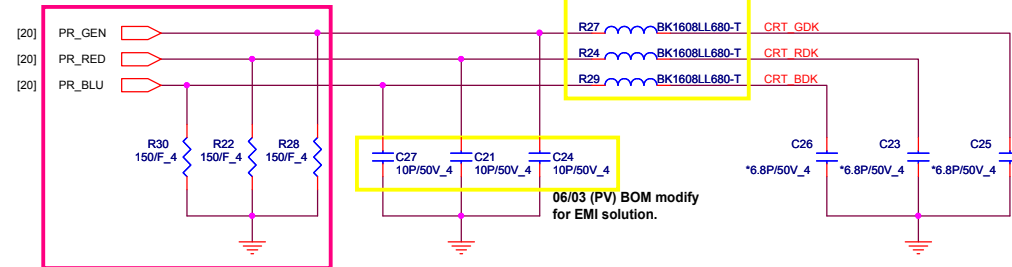


QT6 Modified-0117

CPU FAN

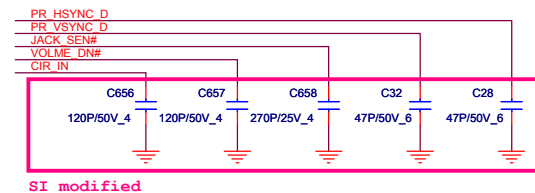
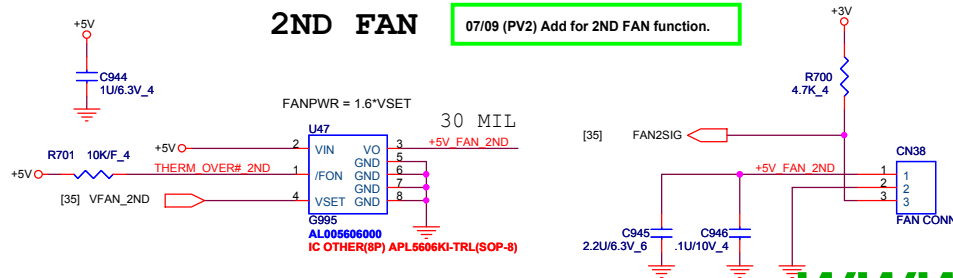


QT6 modified-0117



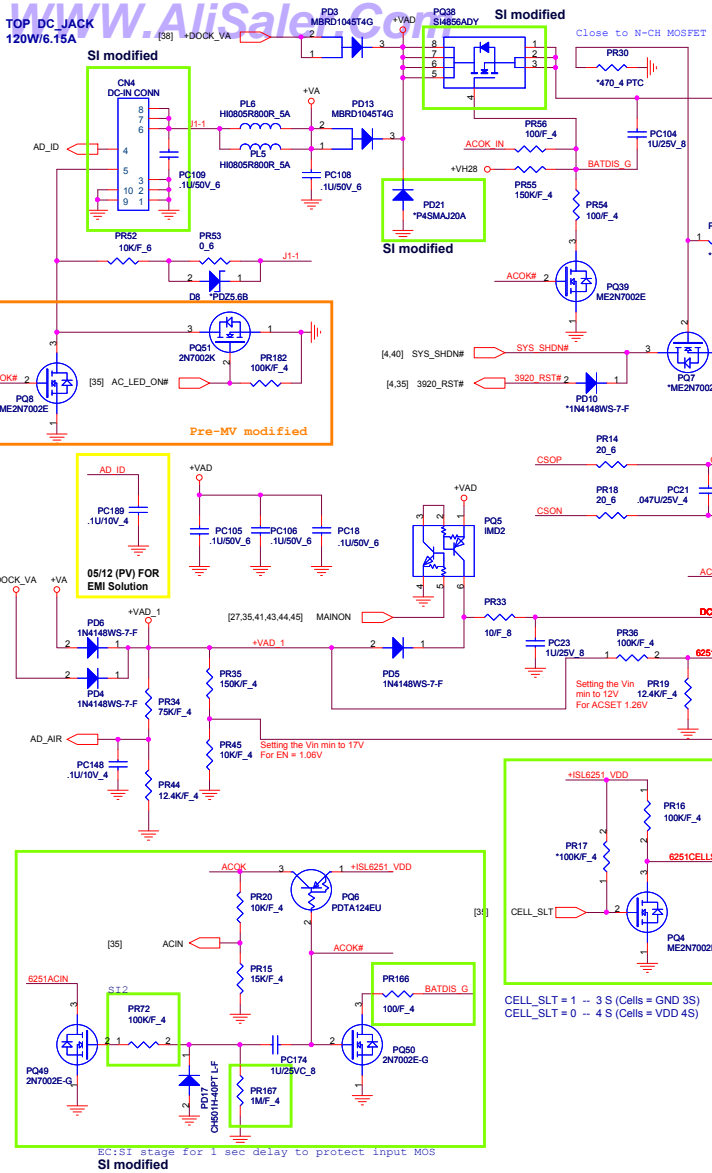
2ND FAN

07/09 (PV2) Add for 2ND FAN function.



PROJECT : UT7
Quanta Computer Inc.

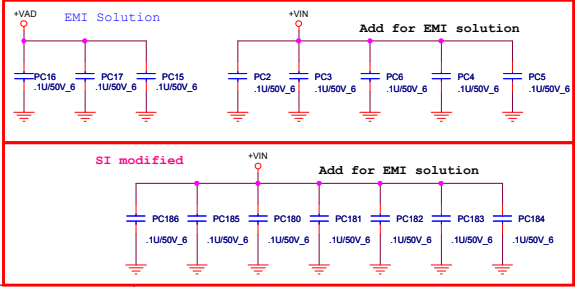
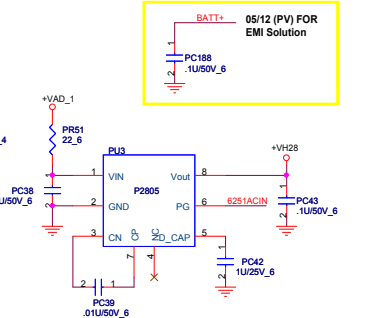
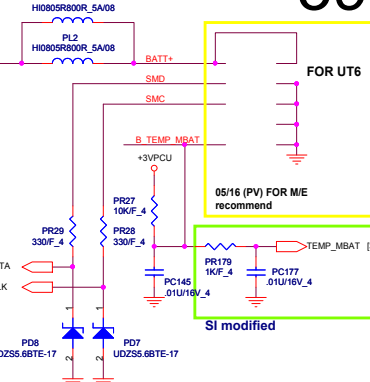
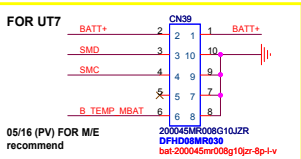
Size	Document Number	Rev
Custom	CABLE DOCKING/FAN	E3A
Date: Friday, July 18, 2008	Sheet 38 of 46	




CN18 BATTERY CONNECTOR TYPE		
MODEL	PART NUMBER	FOOTPRINT
UT6	DFHD08MR015	BAT-BP02083-B09065-7F-9P-V-QT6
UT7	DFHD08MR030	bat-200045mr006g10jzr-8p-l-v

SI modified
Change CN18 to BAT-BP02083-B09065-7F-9P-V-QT6

***UT6, UT7
BATTERY CONNECTOR
SCHEMATIC
LIBRARY IS
DIFFERENT.





PROJECT : UT7
Quanta Computer Inc.

Size	Document Number	Rev
Custom	Charger (ISL6251)	3A
Date: Friday, July 18, 2008		Sheet 39 of 46

40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V ALW

Place these CAPs close to FETs

Pre-MV modified

Place these CAPs close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

5VPCU

+5V_ALWP

PC135 .1U/10V_4

PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

PR83 *0_4

PR84 0_4

PC20 FDS8884

5V LX

5V DH

5V DL

PC136 330U/6.3V_6X5.8

PR137 309K/F_4

PC127 .1U/50V_6

PC126 .1U/10V_4

PC122 1U/6.3V_4

DEL PR72 (47ohm_6)

PC66 4.7U/25V_8

PC65 1U/6.3V_4

PC85 1U/6.3V_4

PC87 1000P/50V_4

PC88 .1U/50V_6

PC93 10U/25V_12

PR183 *0_4

PD22 PDZ5.6B

PR133 1K_4

PR129 150K/F_4

DEL PR78 0_6

PU4 ISL6237IRZ-T

REFIN2

ILIM2

OUT2

SKIP

PGOOD2

ON2

DH2

LX2

REF

TON

VCC

ONLDC

NC

IN

LDO

BYP

OUT1

FB1

ILIM1

PGOOD1

ON1

DH1

LX1

PAD

BS1

BS2

PGND

AGND

VDD

DL1

DL2

PR136 232K/F_4

PGOOD1

PC80 .1U/50V_6

PR96 4.7_6

PR143 4.7_6

PC72 1U/50V_6

PR144 *SHORT-1A

PC81 1U/6.3V_4

PC77 .01U/50V_6

PD11

BAT54SPT

PD12

BAT54SPT

PC82 .1U/25V_8

PR93 100K/F_4

PC85 2.2U/50V_8

+12VALW

+5VALW

PR148 *2.2_8

PC132 *1500P/50V_4

PR85 0_4

PC133 .1U/10V_4

PC134 330U/6.3V_6X5.8

PR87 *0_4

PR149 *2.2_8

PC137 *1500P/50V_4

PR83 *0_4

PR84 0_4

PC135 .1U/10V_4

PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

PR83 *0_4

PR84 0_4

PC20 FDS8884

5V LX

5V DH

5V DL

PC136 330U/6.3V_6X5.8

PR137 309K/F_4

PC127 .1U/50V_6

PC126 .1U/10V_4

PC122 1U/6.3V_4

DEL PR72 (47ohm_6)

PC66 4.7U/25V_8

PC65 1U/6.3V_4

PC85 1U/6.3V_4

PC87 1000P/50V_4

PC88 .1U/50V_6

PC93 10U/25V_12

PR183 *0_4

PD22 PDZ5.6B

PR133 1K_4

PR129 150K/F_4

DEL PR78 0_6

PU4 ISL6237IRZ-T

REFIN2

ILIM2

OUT2

SKIP

PGOOD2

ON2

DH2

LX2

REF

TON

VCC

ONLDC

NC

IN

LDO

BYP

OUT1

FB1

ILIM1

PGOOD1

ON1

DH1

LX1

PAD

BS1

BS2

PGND

AGND

VDD

DL1

DL2

PR136 232K/F_4

PGOOD1

PC80 .1U/50V_6

PR96 4.7_6

PR143 4.7_6

PC72 1U/50V_6

PR144 *SHORT-1A

PC81 1U/6.3V_4

PC77 .01U/50V_6

PD11

BAT54SPT

PD12

BAT54SPT

PC82 .1U/25V_8

PR93 100K/F_4

PC85 2.2U/50V_8

+12VALW

+5VALW

PR148 *2.2_8

PC132 *1500P/50V_4

PR85 0_4

PC133 .1U/10V_4

PC134 330U/6.3V_6X5.8

PR87 *0_4

PR149 *2.2_8

PC137 *1500P/50V_4

PR83 *0_4

PR84 0_4

PC135 .1U/10V_4

PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

PR83 *0_4

PR84 0_4

PC20 FDS8884

5V LX

5V DH

5V DL

PC136 330U/6.3V_6X5.8

PR137 309K/F_4

PC127 .1U/50V_6

PC126 .1U/10V_4

PC122 1U/6.3V_4

DEL PR72 (47ohm_6)

PC66 4.7U/25V_8

PC65 1U/6.3V_4

PC85 1U/6.3V_4

PC87 1000P/50V_4

PC88 .1U/50V_6

PC93 10U/25V_12

PR183 *0_4

PD22 PDZ5.6B

PR133 1K_4

PR129 150K/F_4

DEL PR78 0_6

PU4 ISL6237IRZ-T

REFIN2

ILIM2

OUT2

SKIP

PGOOD2

ON2

DH2

LX2

REF

TON

VCC

ONLDC

NC

IN

LDO

BYP

OUT1

FB1

ILIM1

PGOOD1

ON1

DH1

LX1

PAD

BS1

BS2

PGND

AGND

VDD

DL1

DL2

PR136 232K/F_4

PGOOD1

PC80 .1U/50V_6

PR96 4.7_6

PR143 4.7_6

PC72 1U/50V_6

PR144 *SHORT-1A

PC81 1U/6.3V_4

PC77 .01U/50V_6

PD11

BAT54SPT

PD12

BAT54SPT

PC82 .1U/25V_8

PR93 100K/F_4

PC85 2.2U/50V_8

+12VALW

+5VALW

PR148 *2.2_8

PC132 *1500P/50V_4

PR85 0_4

PC133 .1U/10V_4

PC134 330U/6.3V_6X5.8

PR87 *0_4

PR149 *2.2_8

PC137 *1500P/50V_4

PR83 *0_4

PR84 0_4

PC135 .1U/10V_4

PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

PR83 *0_4

PR84 0_4

PC20 FDS8884

5V LX

5V DH

5V DL

PC136 330U/6.3V_6X5.8

PR137 309K/F_4

PC127 .1U/50V_6

PC126 .1U/10V_4

PC122 1U/6.3V_4

DEL PR72 (47ohm_6)

PC66 4.7U/25V_8

PC65 1U/6.3V_4

PC85 1U/6.3V_4

PC87 1000P/50V_4

PC88 .1U/50V_6

PC93 10U/25V_12

PR183 *0_4

PD22 PDZ5.6B

PR1

40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V ALW

Place these CAPS close to FETs

Pre-MV modified

Place these CAPS close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

[4,39] SYS_SHDN#

PROJECT : UT7
Quanta Computer Inc.

NB5

Size B
Document Number
+5V/+3V (ISL6237)
Rev 3A

Date: Friday, July 18, 2008 Sheet 40 of 46

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40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V ALW

Place these CAPS close to FETs

Pre-MV modified

Place these CAPS close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

Key components and labels include:

- ICs: PQ20 FDS8884, PQ24 SI4686DY-T1-E3, PQ16 FDS8884, PQ19 FDS4410A, PU4 ISL6237IRZ-T.
- Resistors: PR183, PD22, PR133, PR129, PR78, PC127, PC65, PC126, PC122, PR136, PR137, PR148, PR85, PR87, PR81, PR93, PR96, PR144, PC82, PC78, PC85, PC81, PC77, PC88, PC87, PC93, PC135, PC136, PC137, PC132, PC133, PC134, PC79, PC76, PC83, PC66, PC72, PC70, PC71, PC73, PC74, PC75, PC76, PC77, PC78, PC79, PC80, PC81, PC82, PC83, PC84, PC85, PC86, PC87, PC88, PC89, PC90, PC91, PC92, PC93, PC94, PC95, PC96, PC97, PC98, PC99, PC100.
- Capacitors: PC136, PC137, PC132, PC133, PC134, PC79, PC76, PC83, PC66, PC72, PC70, PC71, PC73, PC74, PC75, PC76, PC77, PC78, PC79, PC80, PC81, PC82, PC83, PC84, PC85, PC86, PC87, PC88, PC89, PC90, PC91, PC92, PC93, PC94, PC95, PC96, PC97, PC98, PC99, PC100.
- Inductors: PL10, PL9.
- Diodes: PD11, PD12, BAT54SPT.
- Other: DEL PR72 (47ohm_6), DEL PR76, PR80, PR134, HWPG [35,41,43,44], [4,39] SYS_SHDN#.

PROJECT : UT7
Quanta Computer Inc.

NB5

Size B
Document Number
+5V/+3V (ISL6237)
Date: Friday, July 18, 2008
Sheet 40 of 46
Rev 3A

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40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V ALW

Place these CAPS close to FETs

Pre-MV modified

Place these CAPS close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

Key components and labels include:

- ICs: PQ20 FDS8884, PQ24 SI4686DY-T1-E3, PQ16 FDS8884, PQ19 FDS4410A, PU4 ISL6237IRZ-T.
- Resistors: PR183, PD22, PR133, PR129, PR78, PC127, PC65, PC126, PC122, PR136, PR137, PR148, PR85, PR87, PR91, PR93, PC82, PC78, PC85, PR144, PC81, PC77, PR134, PR133, PR132, PR131, PR130, PR129, PR128, PR127, PR126, PR125, PR124, PR123, PR122, PR121, PR120, PR119, PR118, PR117, PR116, PR115, PR114, PR113, PR112, PR111, PR110, PR109, PR108, PR107, PR106, PR105, PR104, PR103, PR102, PR101, PR100, PR99, PR98, PR97, PR96, PR95, PR94, PR93, PR92, PR91, PR90, PR89, PR88, PR87, PR86, PR85, PR84, PR83, PR82, PR81, PR80, PR79, PR78, PR77, PR76, PR75, PR74, PR73, PR72, PR71, PR70, PR69, PR68, PR67, PR66, PR65, PR64, PR63, PR62, PR61, PR60, PR59, PR58, PR57, PR56, PR55, PR54, PR53, PR52, PR51, PR50, PR49, PR48, PR47, PR46, PR45, PR44, PR43, PR42, PR41, PR40, PR39, PR38, PR37, PR36, PR35, PR34, PR33, PR32, PR31, PR30, PR29, PR28, PR27, PR26, PR25, PR24, PR23, PR22, PR21, PR20, PR19, PR18, PR17, PR16, PR15, PR14, PR13, PR12, PR11, PR10, PR9, PR8, PR7, PR6, PR5, PR4, PR3, PR2, PR1, PR0.
- Capacitors: PC93, PC87, PC88, PC136, PC135, PC134, PC133, PC132, PC131, PC130, PC129, PC128, PC127, PC126, PC125, PC124, PC123, PC122, PC121, PC120, PC119, PC118, PC117, PC116, PC115, PC114, PC113, PC112, PC111, PC110, PC109, PC108, PC107, PC106, PC105, PC104, PC103, PC102, PC101, PC100, PC99, PC98, PC97, PC96, PC95, PC94, PC93, PC92, PC91, PC90, PC89, PC88, PC87, PC86, PC85, PC84, PC83, PC82, PC81, PC80, PC79, PC78, PC77, PC76, PC75, PC74, PC73, PC72, PC71, PC70, PC69, PC68, PC67, PC66, PC65, PC64, PC63, PC62, PC61, PC60, PC59, PC58, PC57, PC56, PC55, PC54, PC53, PC52, PC51, PC50, PC49, PC48, PC47, PC46, PC45, PC44, PC43, PC42, PC41, PC40, PC39, PC38, PC37, PC36, PC35, PC34, PC33, PC32, PC31, PC30, PC29, PC28, PC27, PC26, PC25, PC24, PC23, PC22, PC21, PC20, PC19, PC18, PC17, PC16, PC15, PC14, PC13, PC12, PC11, PC10, PC9, PC8, PC7, PC6, PC5, PC4, PC3, PC2, PC1, PC0.
- Inductors: PL10, PL9.
- Diodes: PD11, PD12, PD13, PD14, PD15, PD16, PD17, PD18, PD19, PD20, PD21, PD22, PD23, PD24, PD25, PD26, PD27, PD28, PD29, PD30, PD31, PD32, PD33, PD34, PD35, PD36, PD37, PD38, PD39, PD40, PD41, PD42, PD43, PD44, PD45, PD46, PD47, PD48, PD49, PD50, PD51, PD52, PD53, PD54, PD55, PD56, PD57, PD58, PD59, PD60, PD61, PD62, PD63, PD64, PD65, PD66, PD67, PD68, PD69, PD70, PD71, PD72, PD73, PD74, PD75, PD76, PD77, PD78, PD79, PD80, PD81, PD82, PD83, PD84, PD85, PD86, PD87, PD88, PD89, PD90, PD91, PD92, PD93, PD94, PD95, PD96, PD97, PD98, PD99, PD100.
- Other: [4,39] SYS_SHDN#, HWPG [35,41,43,44].

PROJECT : UT7
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NB5

Size B
Document Number
+5V/+3V (ISL6237)
Rev 3A
Date: Friday, July 18, 2008
Sheet 40 of 46

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40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V ALW

Place these CAPs close to FETs

Pre-MV modified

Place these CAPs close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

5VPCU

+5V_ALWP

PC135 .1U/10V_4

PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

PR83 *0_4

PR84 0_4

PC20 FDS8884

5V LX

5V DH

5V DL

PC136 330U/6.3V_6X5.8

PR137 309K/F_4

PC127 .1U/50V_6

PC126 .1U/10V_4

PC122 1U/6.3V_4

DEL PR72 (47ohm_6)

PC66 4.7U/25V_8

PC65 1U/6.3V_4

PC85 1U/6.3V_4

PC87 1000P/50V_4

PC88 .1U/50V_6

PC93 10U/25V_12

PR183 *0_4

PD22 PDZ5.6B

PR133 1K_4

PR129 150K/F_4

DEL PR78 0_6

DEL PR76,PR80,PR134

PU4 ISL6237IRZ-T

REFIN2

ILIM2

OUT2

SKIP

PGOOD2

ON2

DH2

LX2

REF

VCC

TON

NC

IN

LD

BYP

OUT1

FB1

ILIM1

PGOOD1

ON1

DH1

LX1

PAD

PGND

AGND

BS1

BS2

PR136 232K/F_4

PGOOD1

PC80 .1U/50V_6

PR96 4.7_6

PR143 4.7_6

PC72 1U/50V_6

PR144 *SHORT-1A

PC81 1U/6.3V_4

PC77 .01U/50V_6

PD11

BAT54SPT

PD12

BAT54SPT

PC82 .1U/25V_8

PR93 100K/F_4

PC85 2.2U/50V_8

+5VALW

+12VALW

[4,39] SYS_SHDN#

3V LX

3V DH

3V DL

PL9 2.5uH/7.5A

PR148 *2.2_8

PC132 *1500P/50V_4

PR85 0_4

PC133 .1U/10V_4

PC134 330U/6.3V_6X5.8

PR87 *0_4

PR81 0_4

HWPG [35,41,43,44]

PGOOD2

PGOOD1

3VPCU

+3.3V_ALWP

PC137

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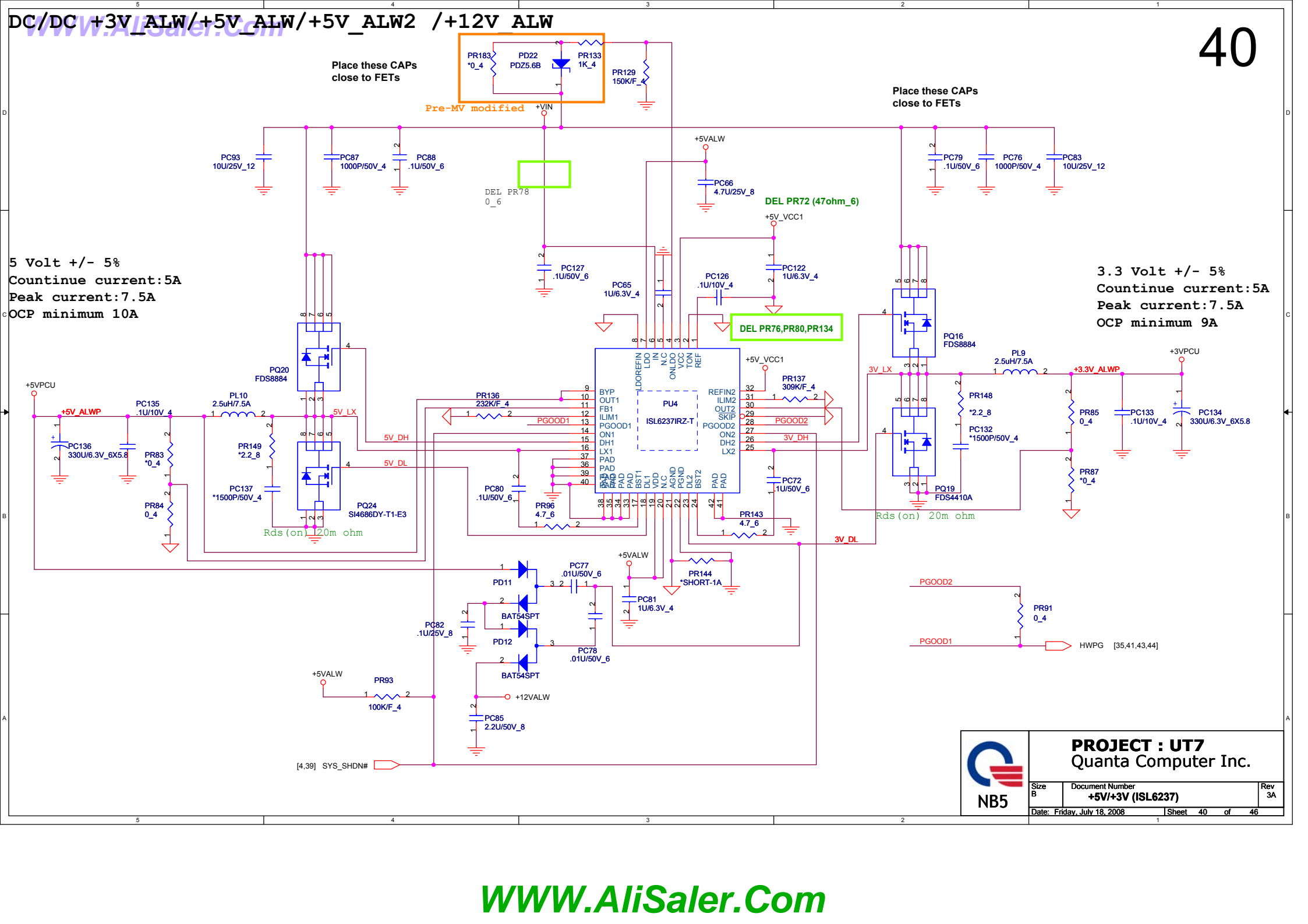
PC392

PC393

PC394

PC395

PC396



40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V ALW

Place these CAPs close to FETs

Pre-MV modified

Place these CAPs close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

Key components and labels include:

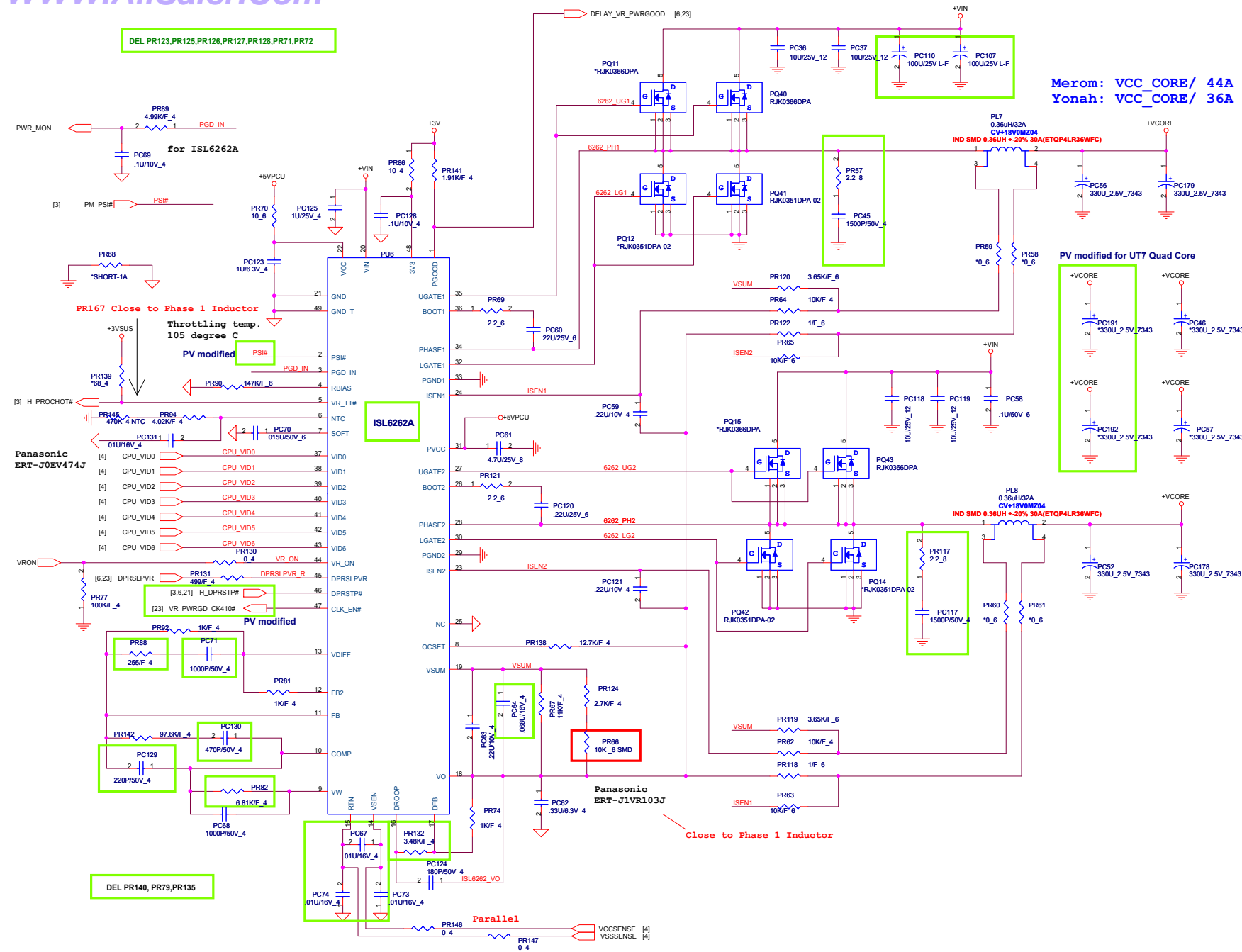
- ICs: PQ20 FDS8884, PQ24 SI4686DY-T1-E3, PQ16 FDS8884, PQ19 FDS4410A, PU4 ISL6237IRZ-T.
- Resistors: PR183, PD22, PR133, PR129, PR78, PC127, PC65, PC126, PC122, PR136, PR137, PR148, PR85, PR87, PR81, PR93, PR96, PR144, PC82, PC78, PC85, PC81, PC77, PC88, PC87, PC93, PC135, PC136, PC137, PC132, PC133, PC134, PC79, PC76, PC83, PC66, PC72, PC70, PC71, PC73, PC74, PC75, PC76, PC77, PC78, PC79, PC80, PC81, PC82, PC83, PC84, PC85, PC86, PC87, PC88, PC89, PC90, PC91, PC92, PC93, PC94, PC95, PC96, PC97, PC98, PC99, PC100.
- Capacitors: PC93, PC87, PC88, PC127, PC65, PC126, PC122, PC136, PC137, PC132, PC133, PC134, PC79, PC76, PC83, PC66, PC72, PC70, PC71, PC73, PC74, PC75, PC76, PC77, PC78, PC79, PC80, PC81, PC82, PC83, PC84, PC85, PC86, PC87, PC88, PC89, PC90, PC91, PC92, PC93, PC94, PC95, PC96, PC97, PC98, PC99, PC100.
- Inductors: PL10, PL9.
- Diodes: PD11, PD12, BAT54SPT.
- Other: DEL PR72 (47ohm_6), DEL PR76, PR80, PR134, HWPG [35,41,43,44], [4,39] SYS_SHDN#.

PROJECT : UT7
Quanta Computer Inc.

NB5

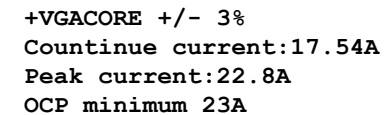
Size B
Document Number
+5V/+3V (ISL6237)
Rev 3A
Date: Friday, July 18, 2008
Sheet 40 of 46

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PROJECT : UT7
Quanta Computer Inc.

Size Custom	Document Number CPU Core (ISL6266A)	Rev 3A
Date: Friday, July 18, 2008	Sheet 42	of 48



800 mils
22.5A

+VGACORE

VREF=2.75V +/-1.5%

NB9P-GS: PR163=392Kohm
Output = 0.9V

NB9M-GE: PR203=590Kohm
NB9P-GS: PR203=768Kohm

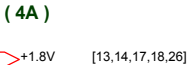
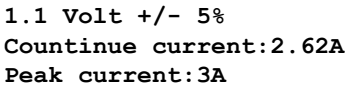
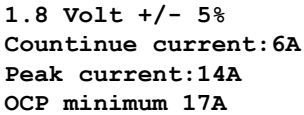
CS45902FB10 RES CHIP 590K 1/16W +-1%(0402)
CS47682FB10 RES CHIP 768K 1/16W +-1%(0402)

VGA_GPIO6	V_PWCNTL		NB9P-GS	NB9M-GS
GPIO6	GPIO5			
Low	Low	MAX BAT	0.9V	0.9V
Low	High	SD DVD	0.9V	0.9V
High	Low	HD DVD	0.9V	0.9V
High	High	MAX PERF	1.05V	1.09V

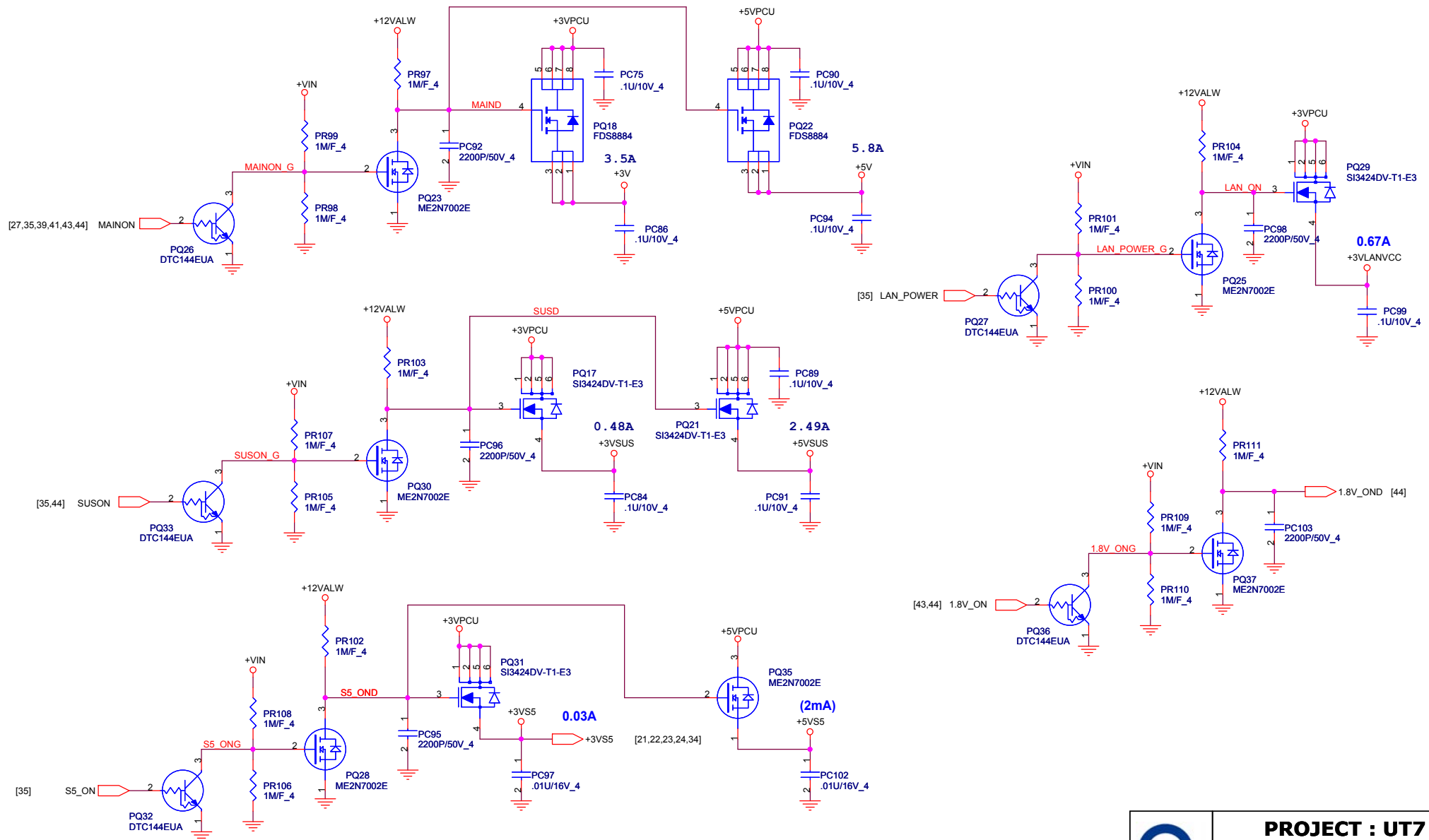


PROJECT : UT7
Quanta Computer Inc.

Size B	Document Number VGA CORE OZ8118	Rev 3A
Date: Friday, July 18, 2008		Sheet 43 of 46



Size B	Document Number 1.8V/DDR_VTER/+1.8v/+1.1V	Rev 3A
Date: Friday, July 18, 2008	Sheet 44 of 46	



PROJECT : UT7
Quanta Computer Inc.

Size B	Document Number DISCHARGE/3VS5/5VS5/LAN	Rev 3A
Date: Friday, July 18, 2008	Sheet 45 of 46	

	Voltage level	AC MODE				DC MODE			
		S0	S3	S4	S5	S0	S3	S4	S5
+3VPCU	3.3V +/- 5%	V	V	V	V	V	V	V	V
+5VPCU	5V +/- 5%	V	V	V	V	V	V	V	V
+3VRTC	3.3V +/- 5%	V	V	V	V	V	V	V	V
+3VS5	3.3V +/- 5%	V	V	V	V	V	V		
+5VS5	5V +/- 5%	V	V	V	V	V	V		
+3VSUS	3.3V +/- 5%	V	V			V	V		
+5VSUS	5V +/- 5%	V	V			V	V		
+1.8VSUS	1.8V +/- 5%	V	V			V	V		
+0.9VSMVTT	0.9V +/- 5%	V	V			V	V		
+1.5V	1.5V +/- 5%	V				V			
+1.05V	1.05V +/- 5%	V				V			
+VCORE	0.9~1.15V	V				V			
+VGA_CORE	0.9~1.2V	V				V			
+VGA1.1V	1.1V +/- 5%	V				V			
+1.8V	1.8V +/- 5%	V				V			
+3VLAVCC	3.3V +/- 5%	V				V			



NB5

PROJECT : UT7
Quanta Computer Inc.

Size
CustomDocument Number
VoltageRev
E3A

Date: Friday, July 18, 2008

Sheet 46 of 46